

Figure 1A

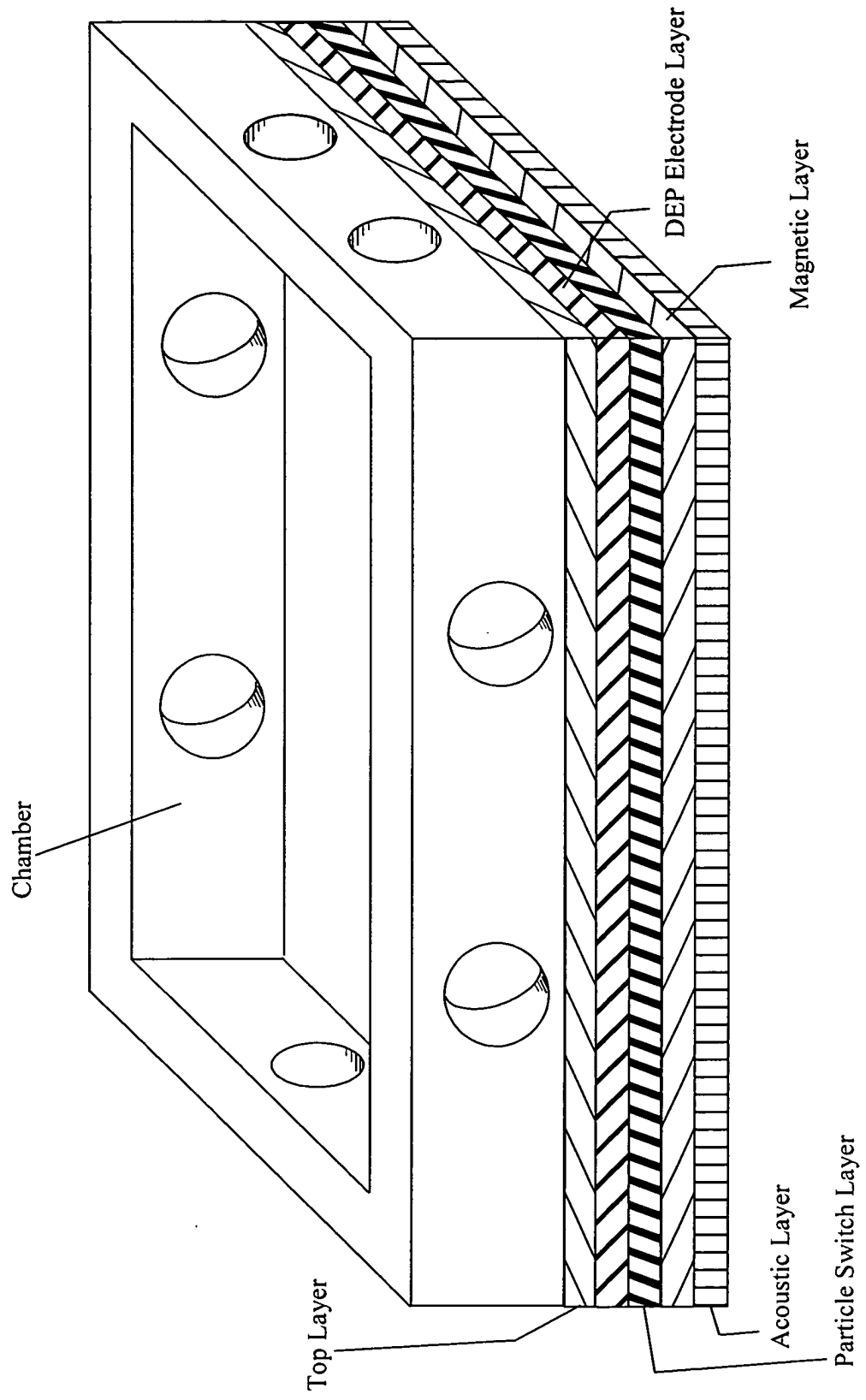


Figure 1B

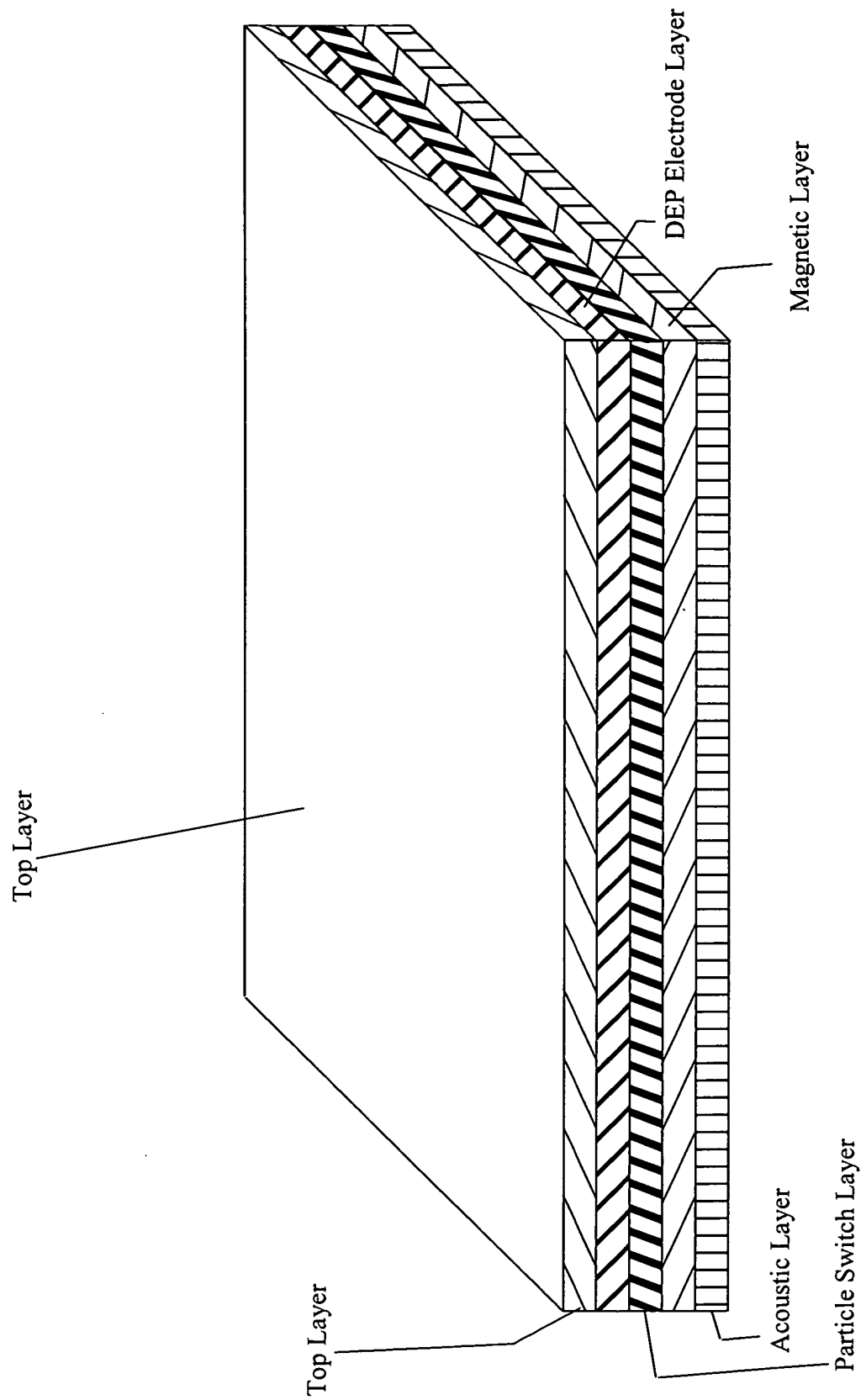


Figure 1C

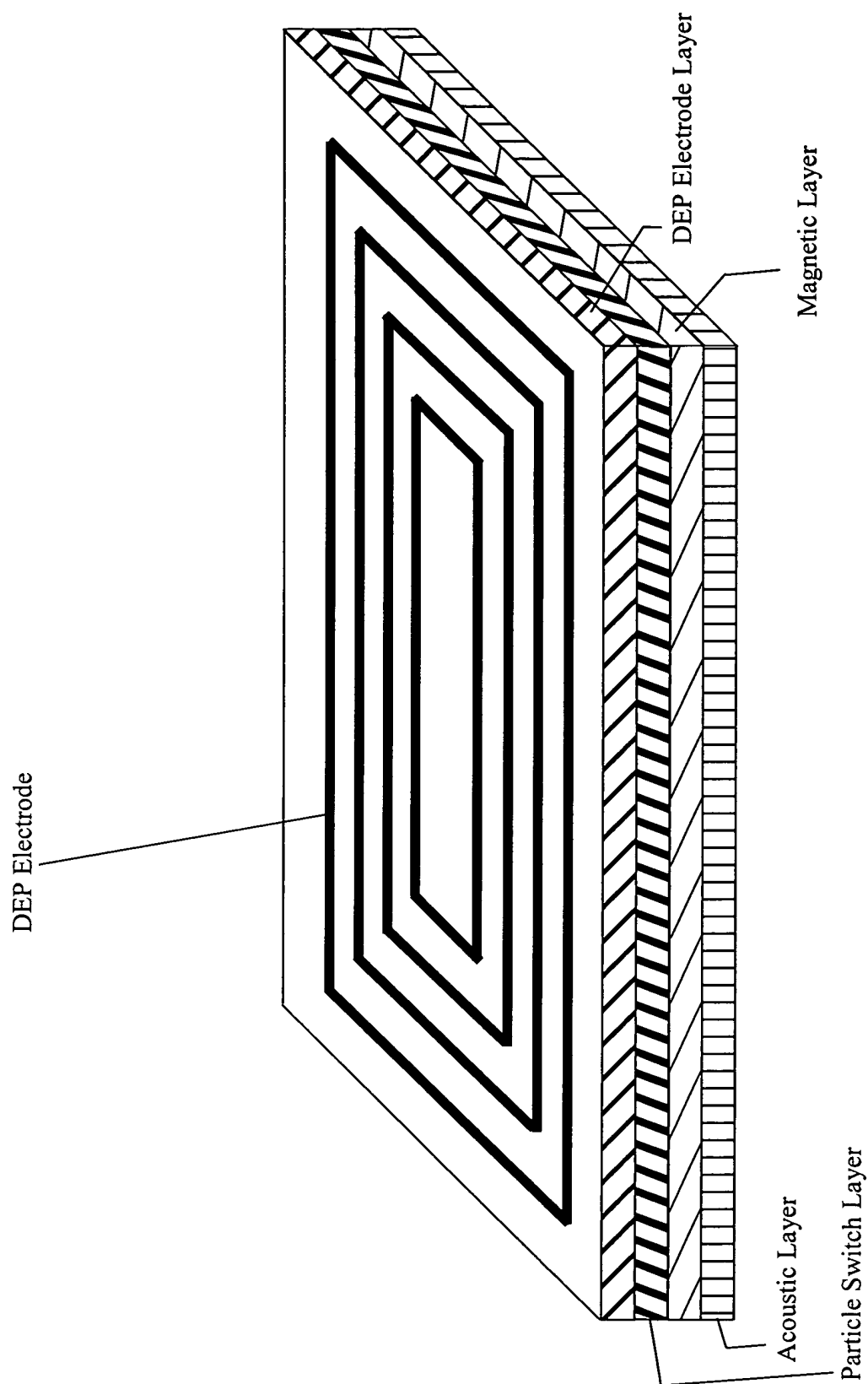
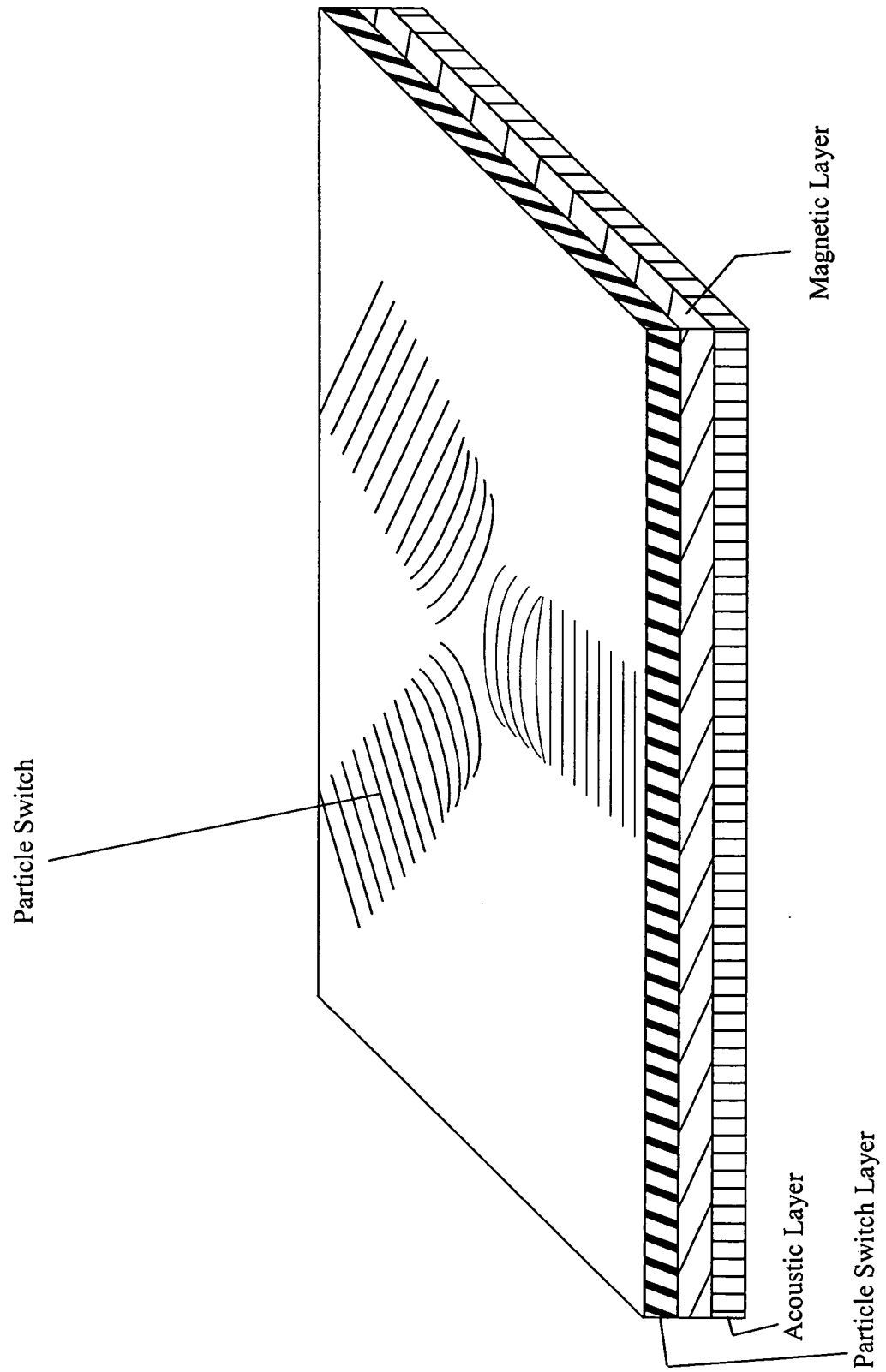


Figure 1D



TOP SECRET

Figure 1E

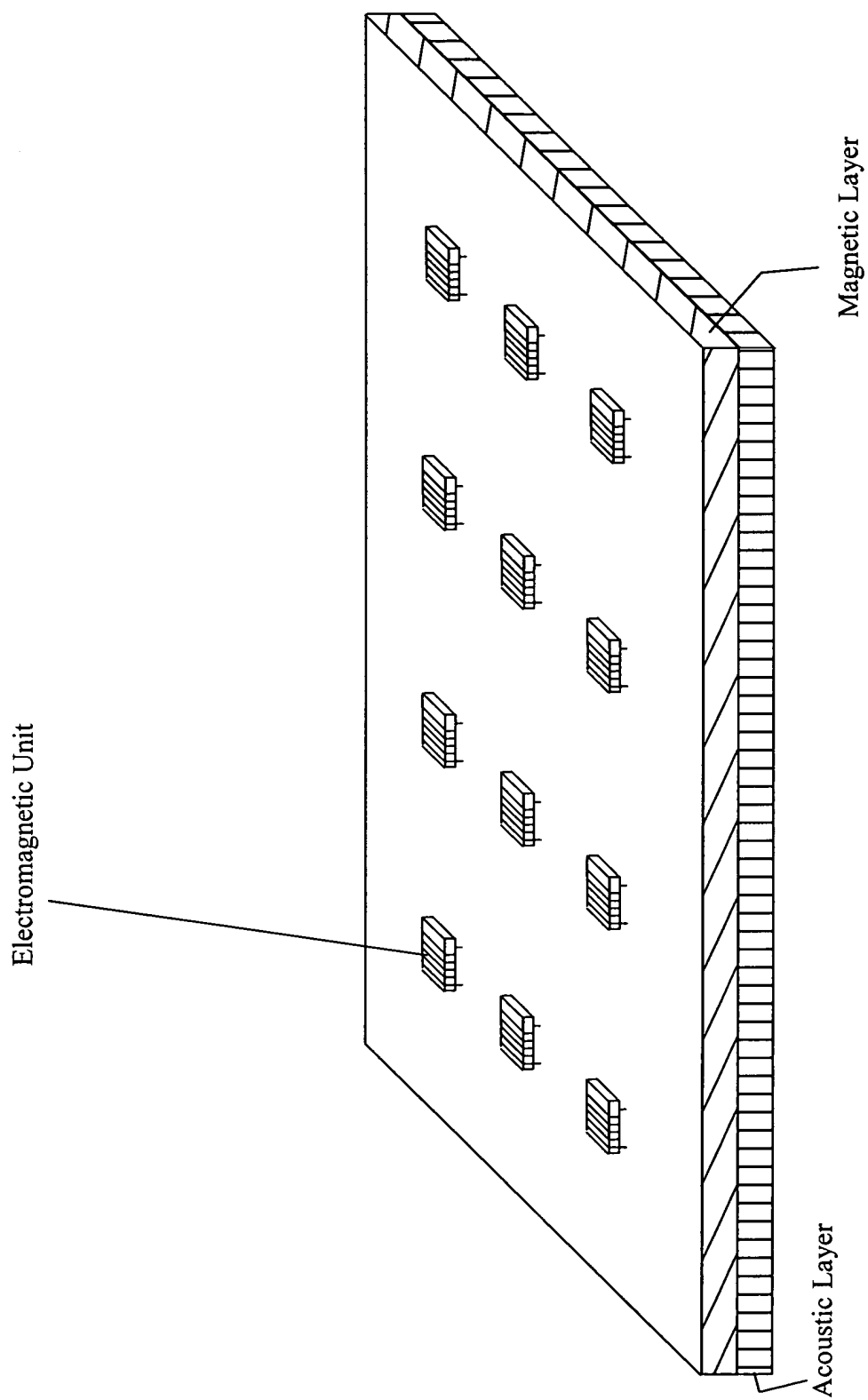


Figure 1F

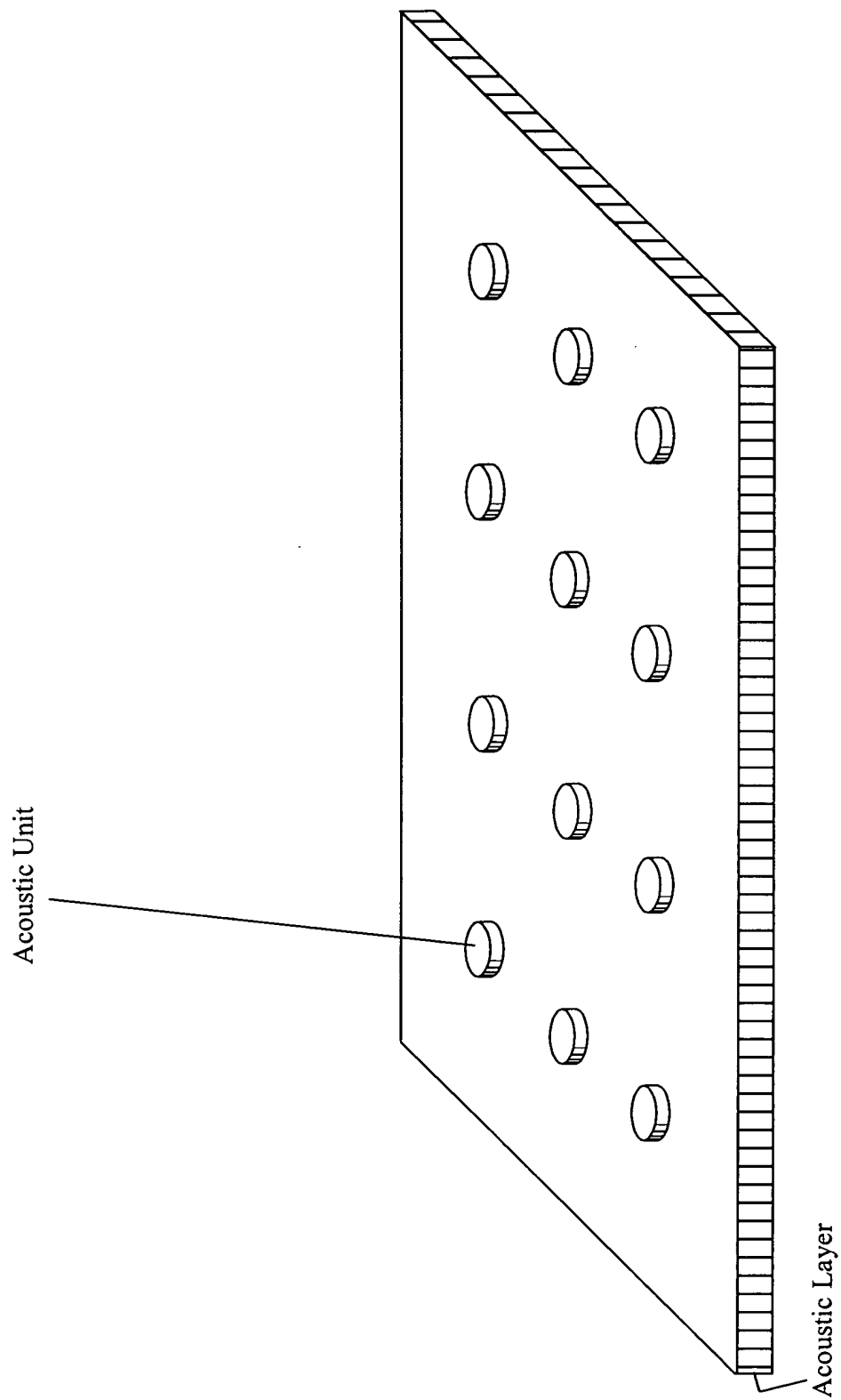


Figure 2A

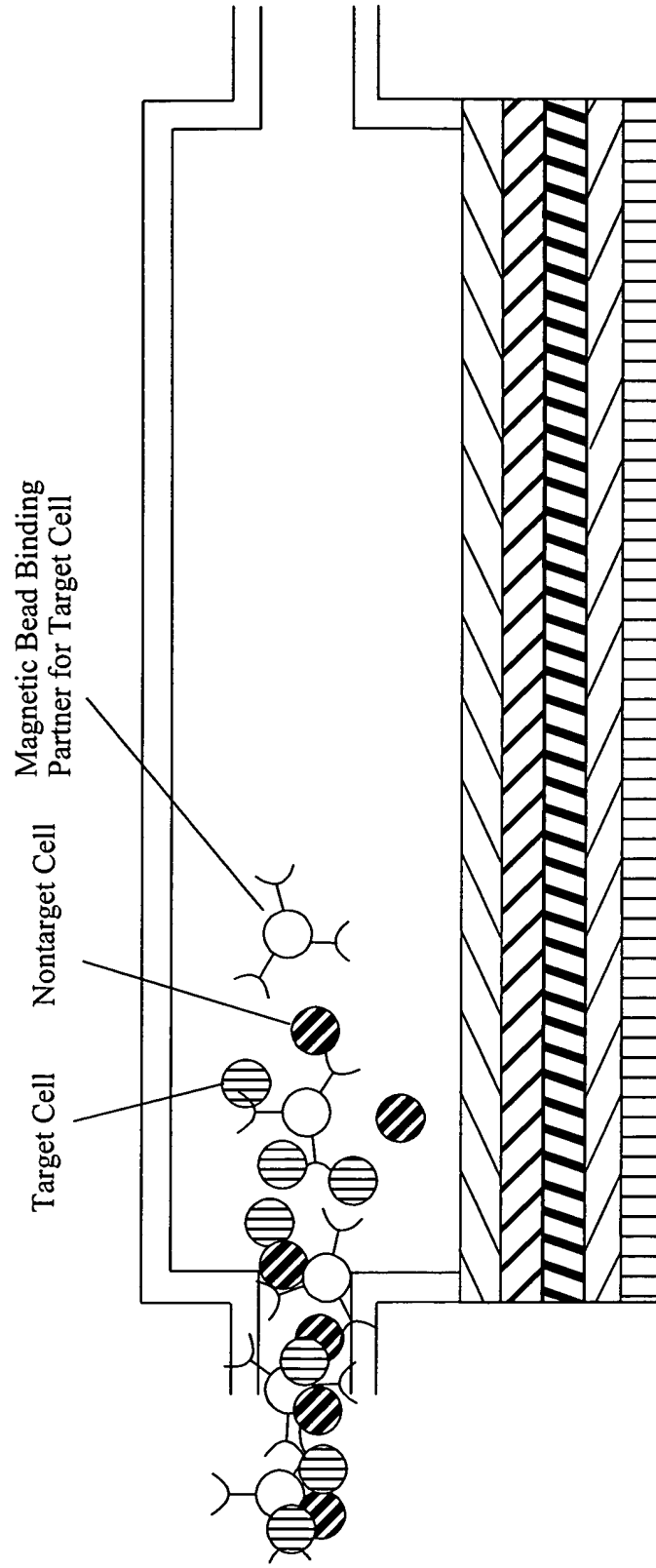


Figure 2B

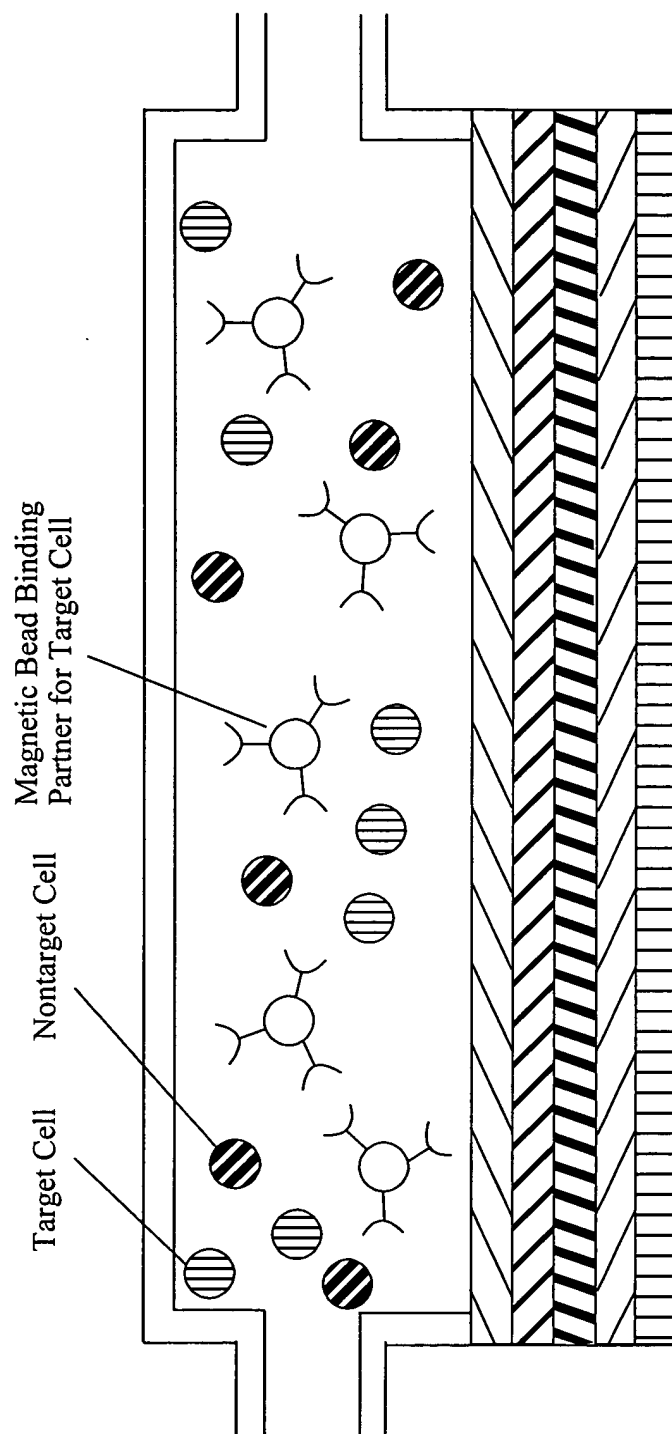


Figure 3

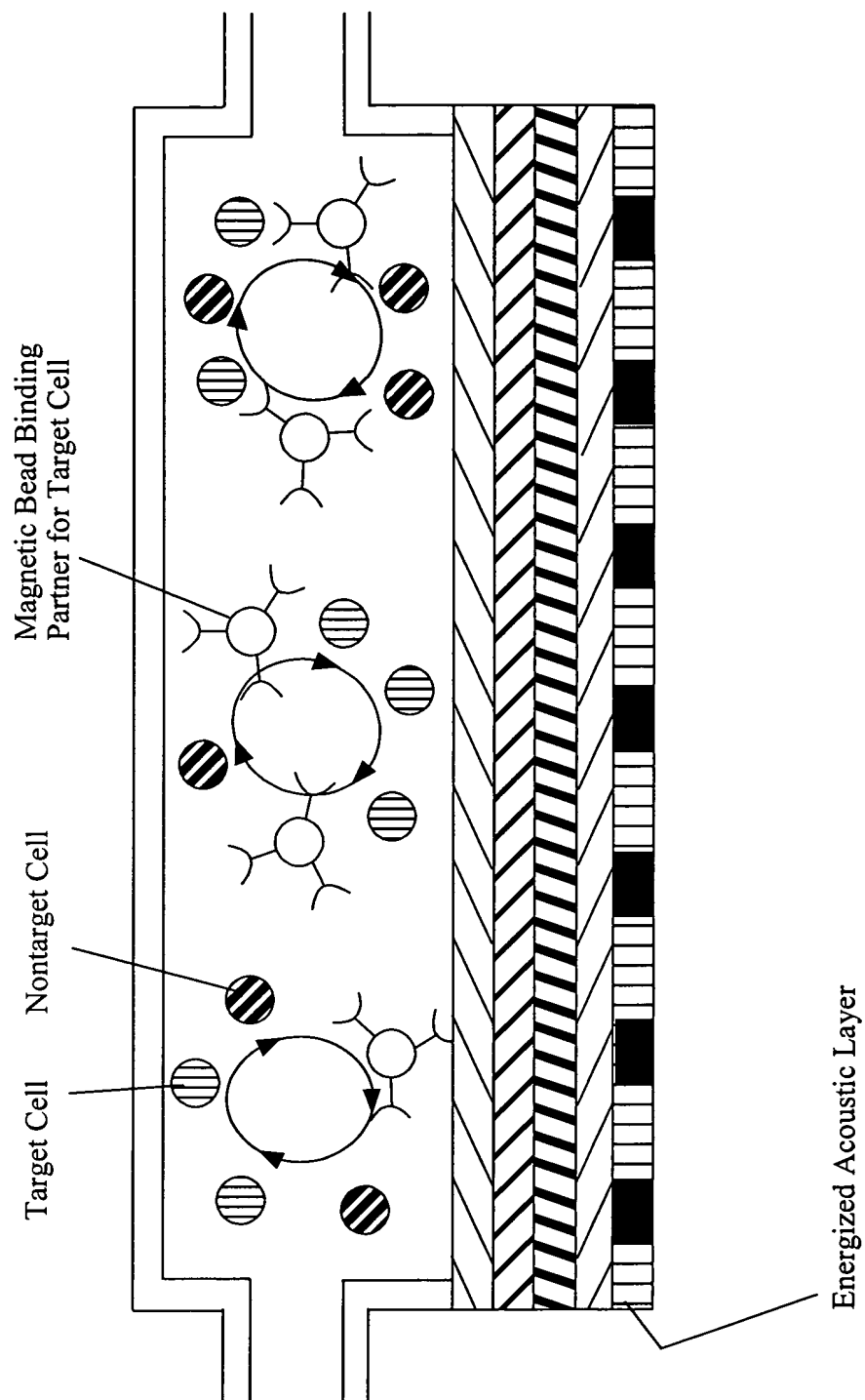


Figure 4

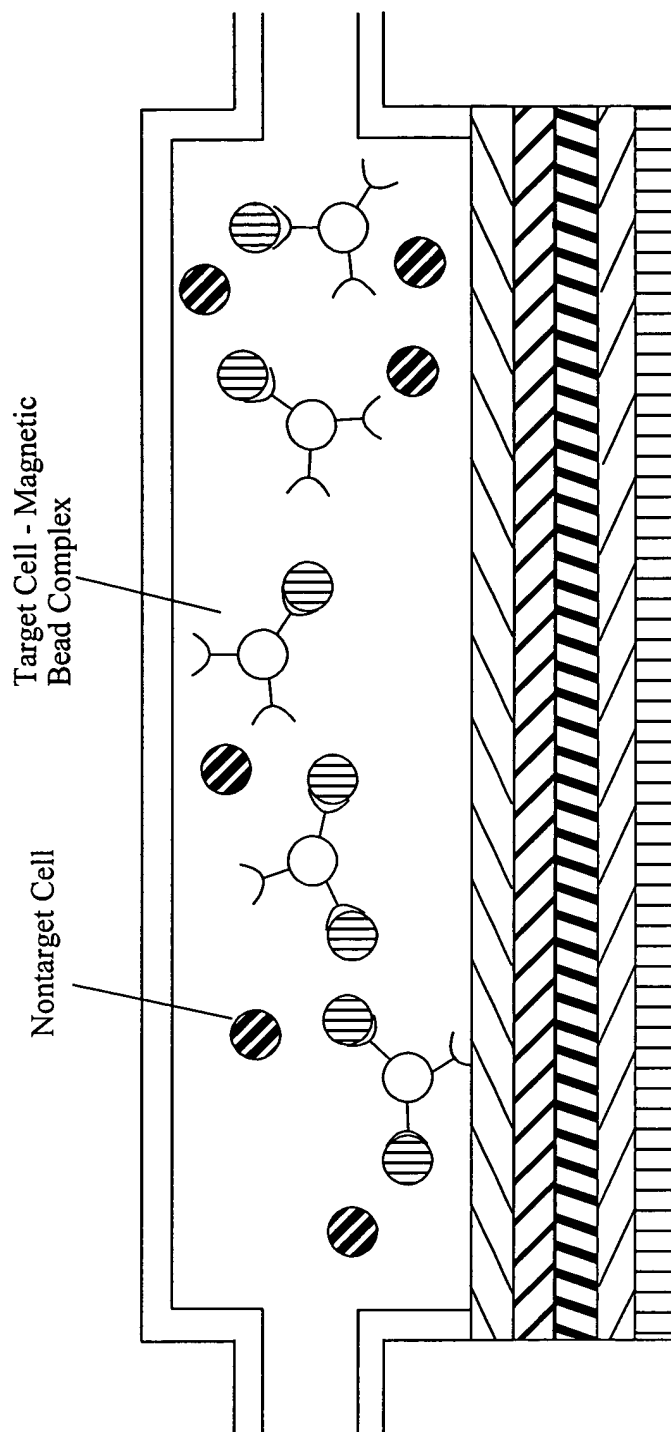


Figure 5A

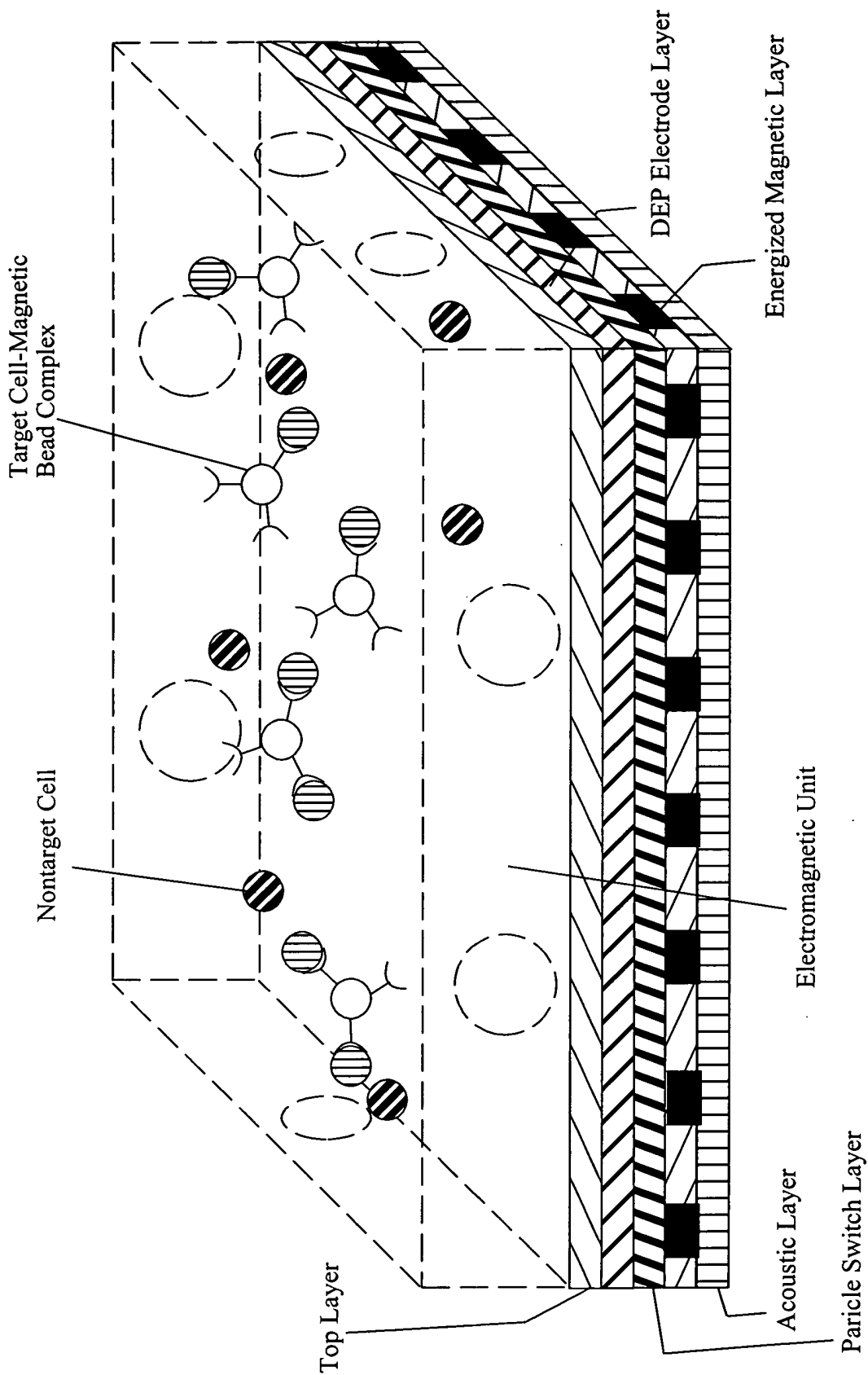


Figure 5B

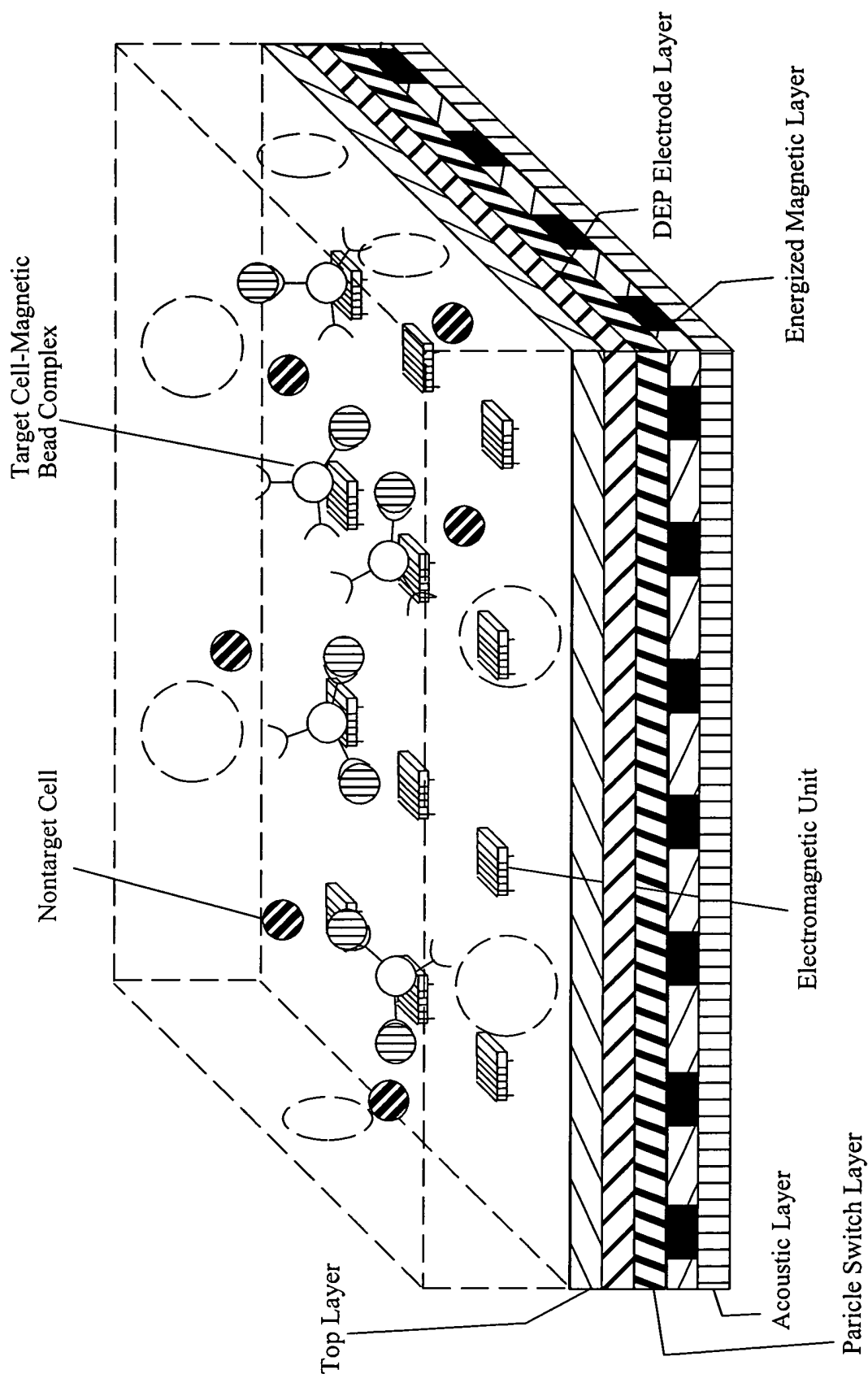


Figure 5C

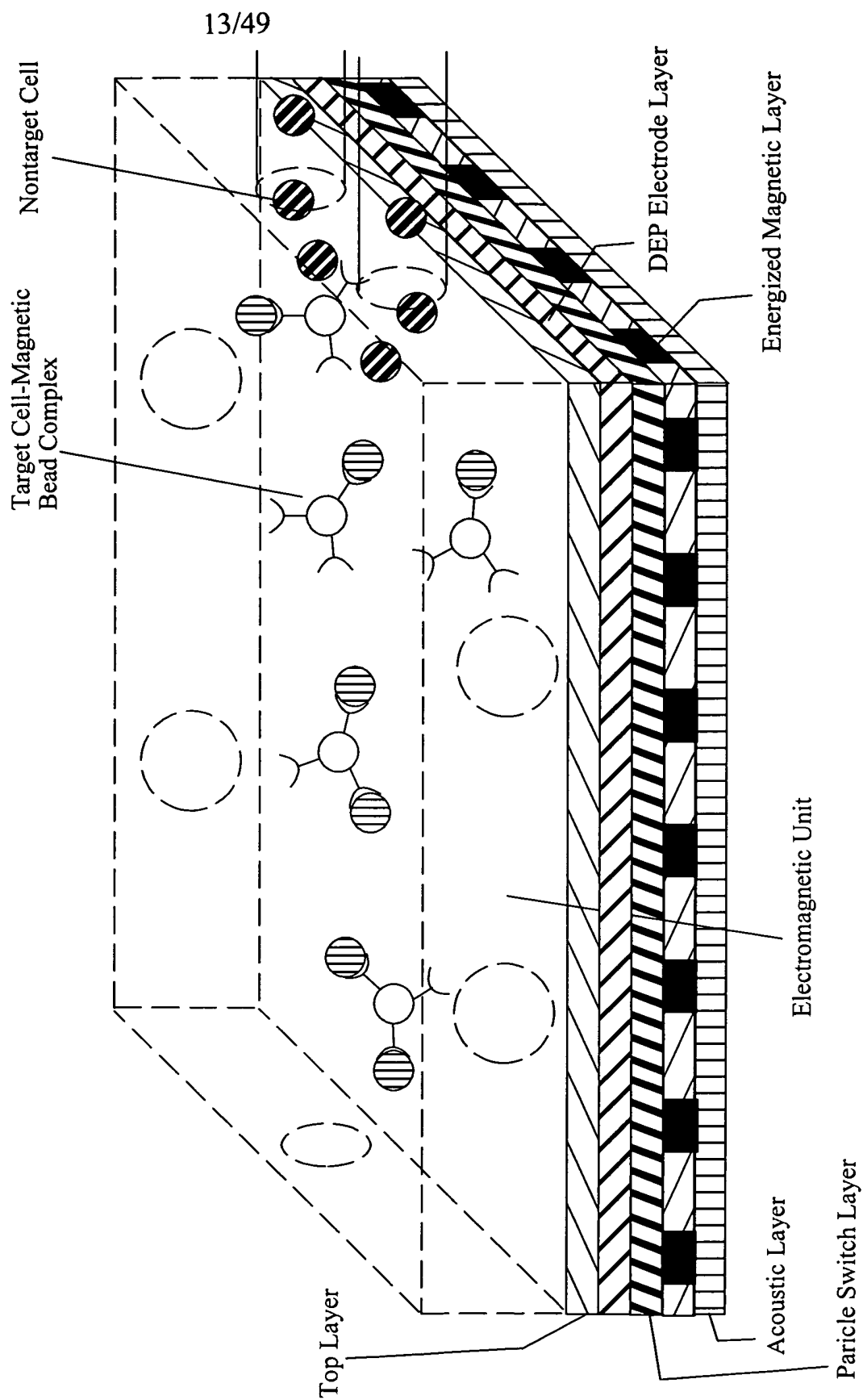


Figure 6

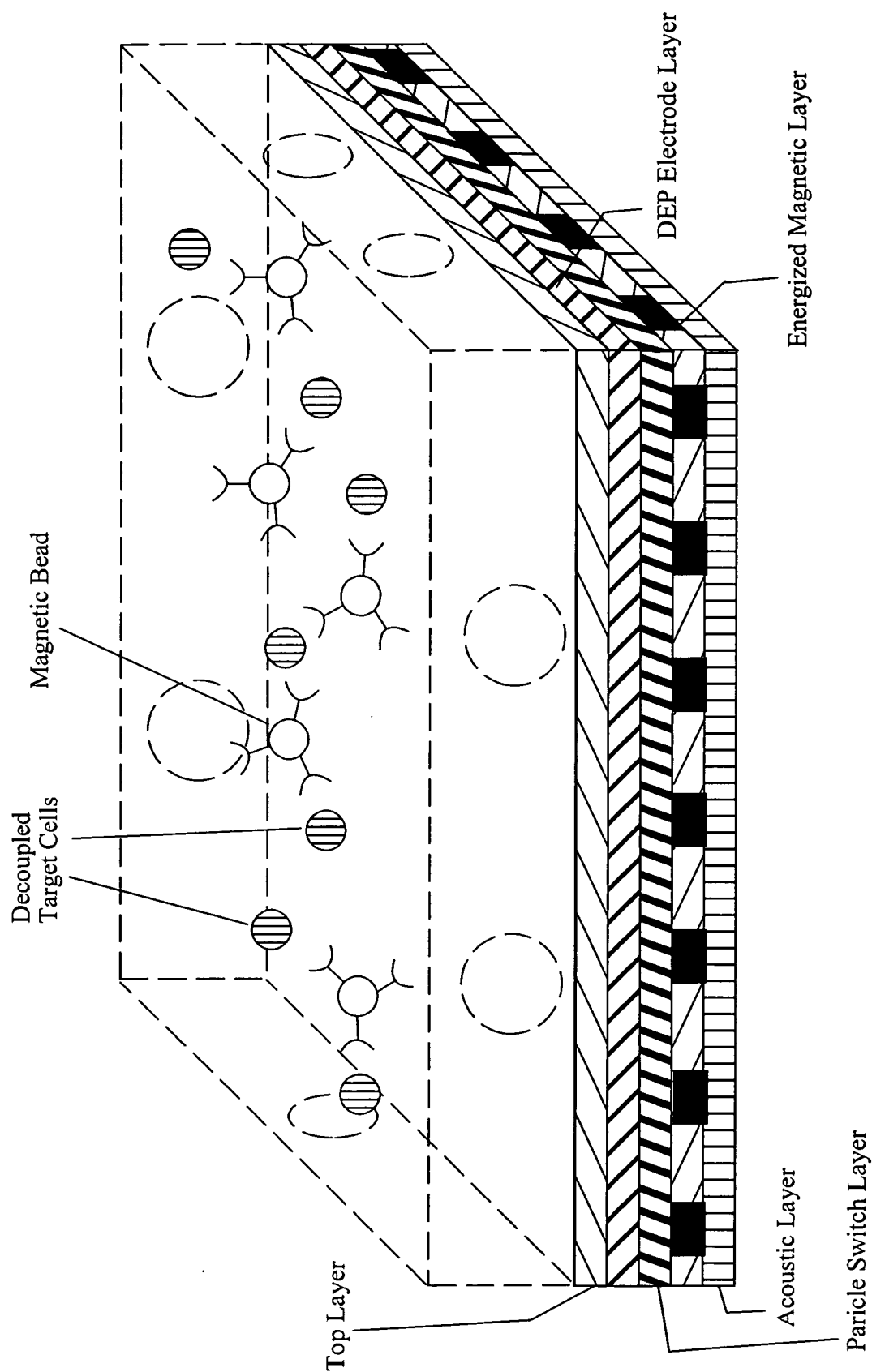


Figure 7A

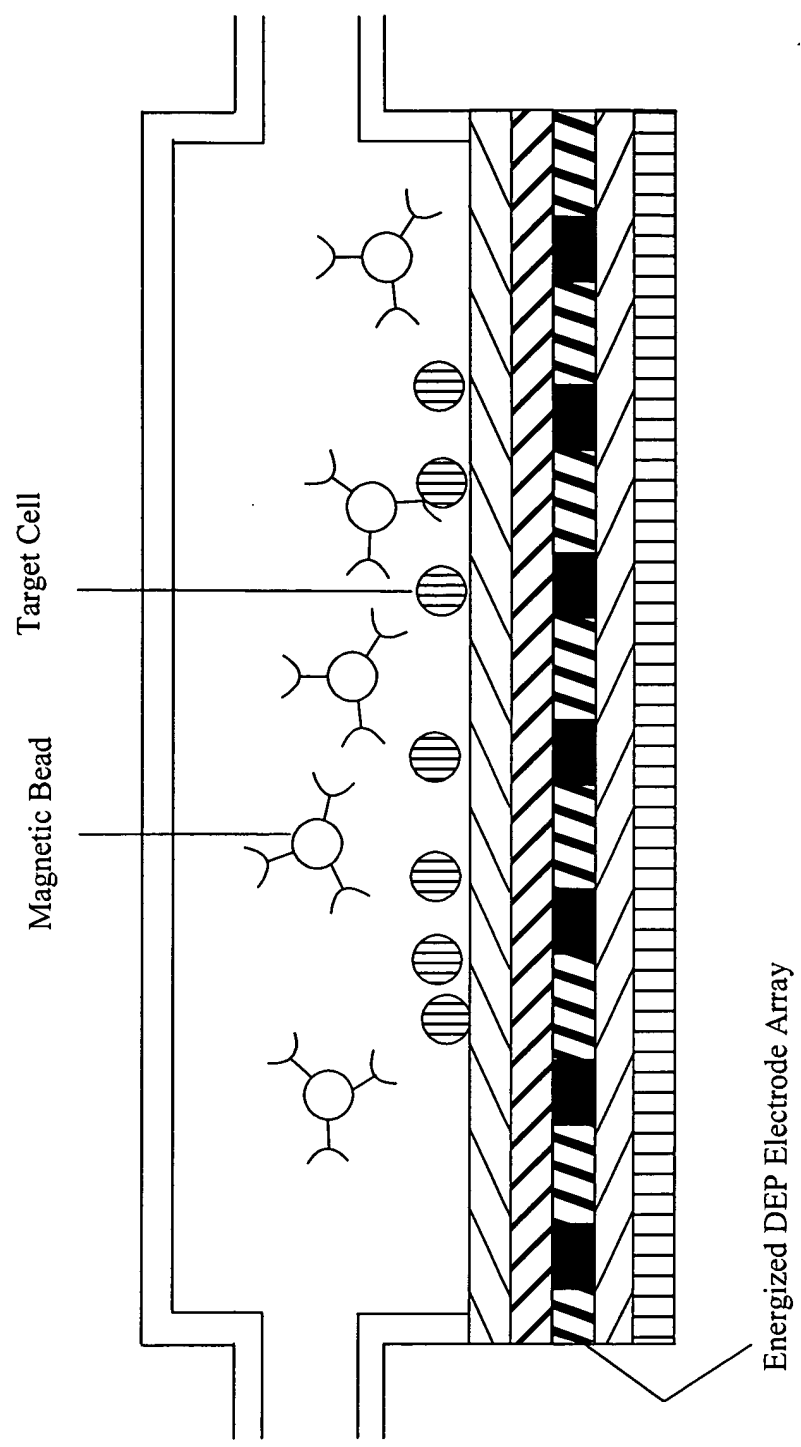


Figure 7B

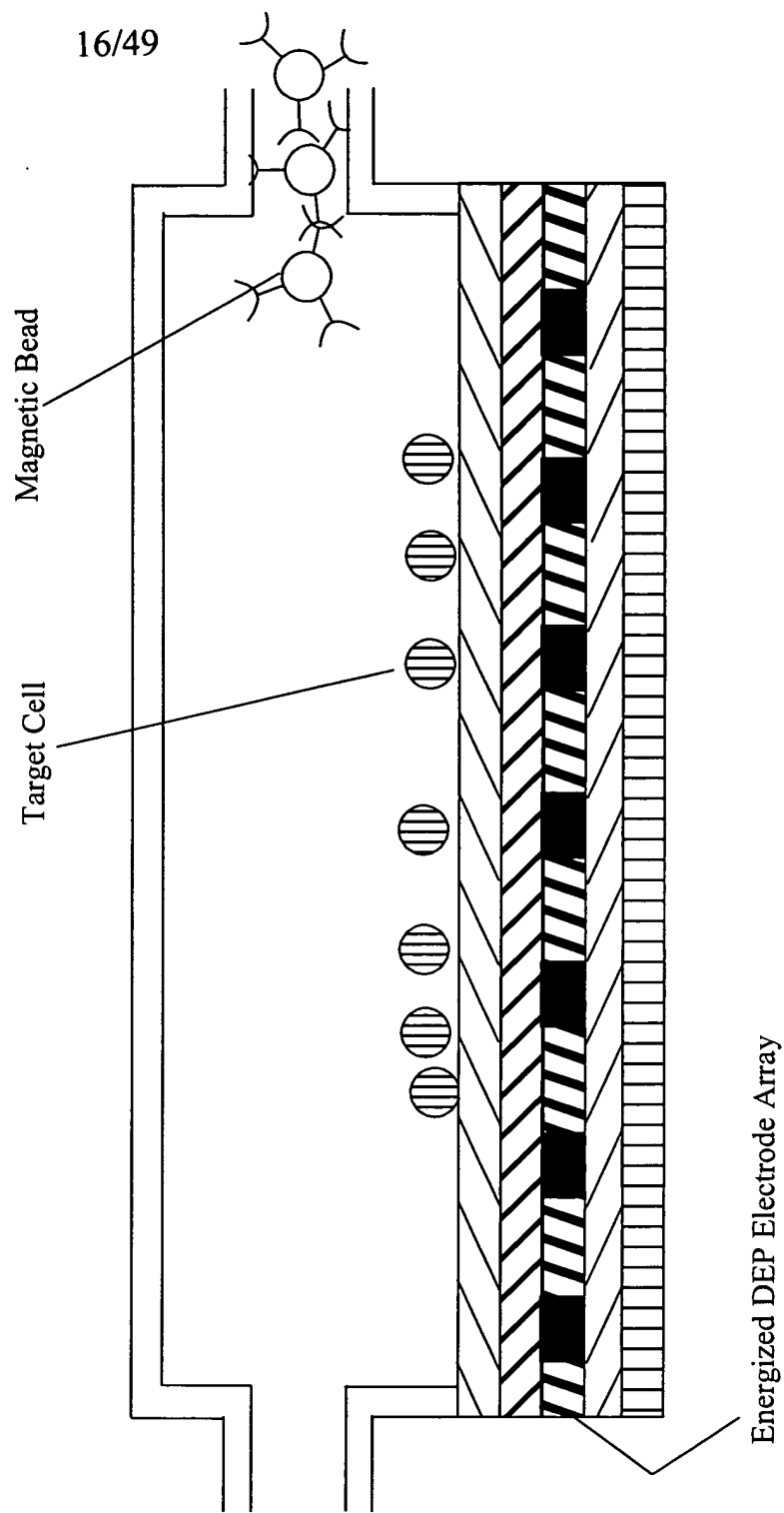


Figure 8

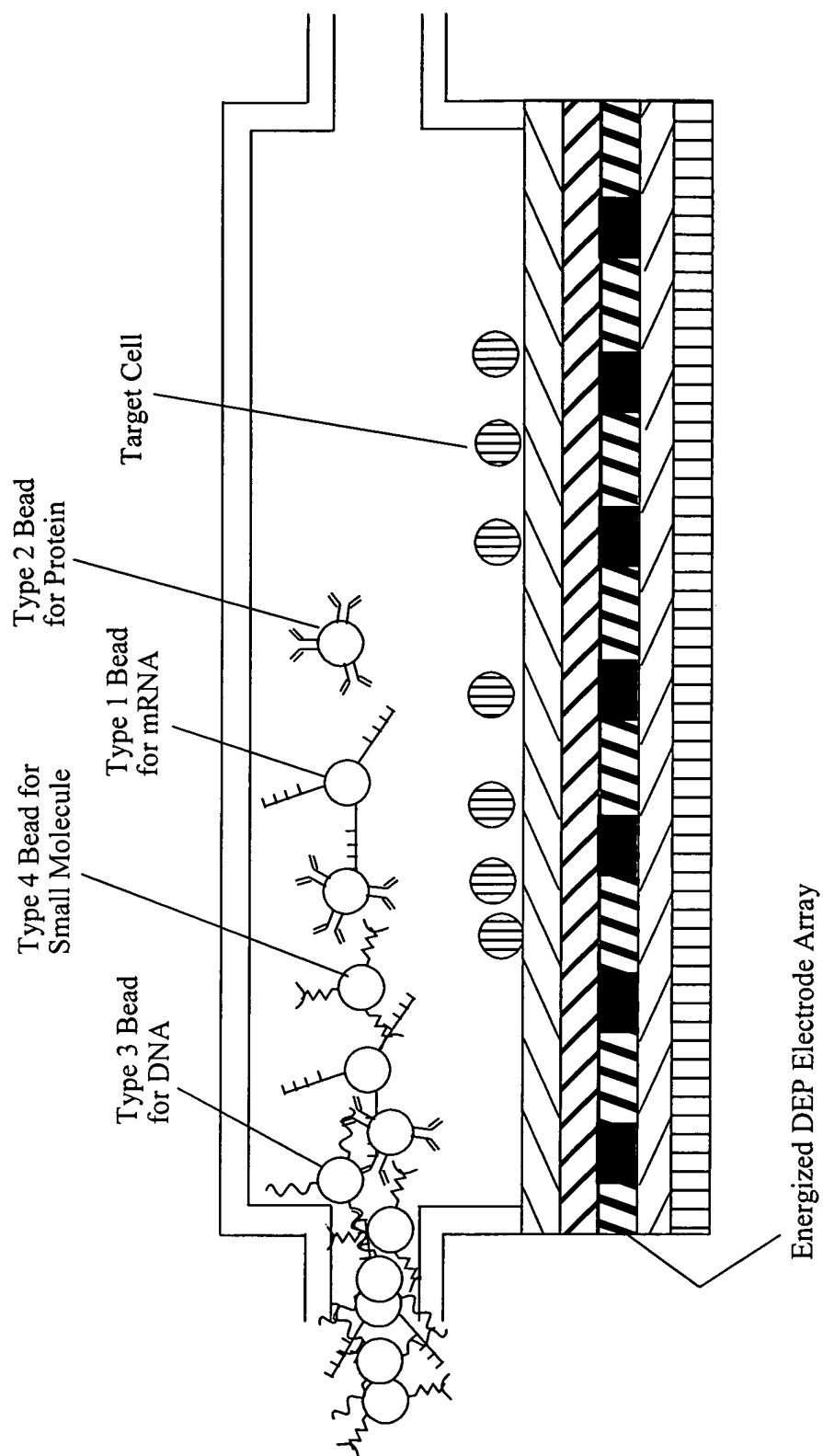


Figure 9A

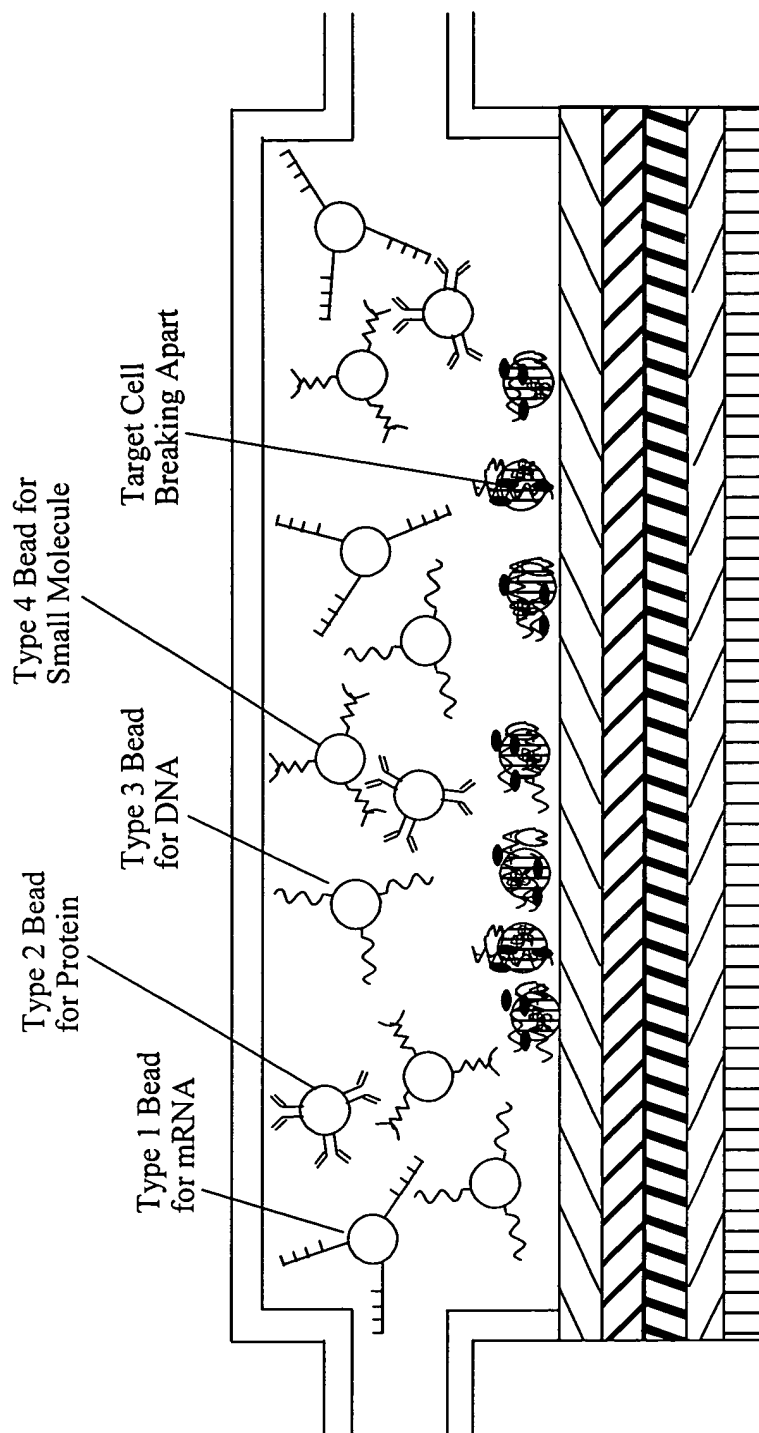


Figure 9B

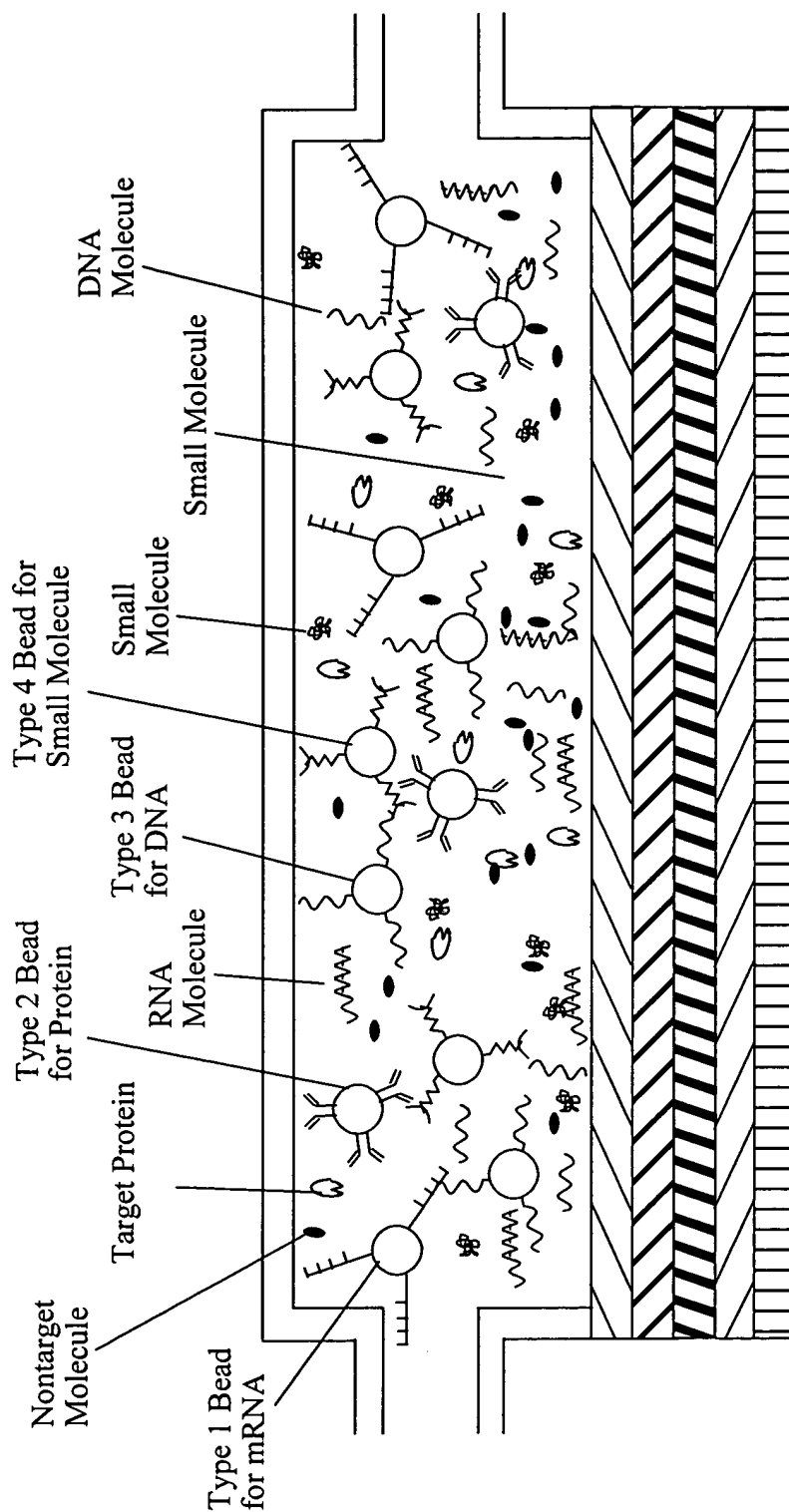


Figure 10

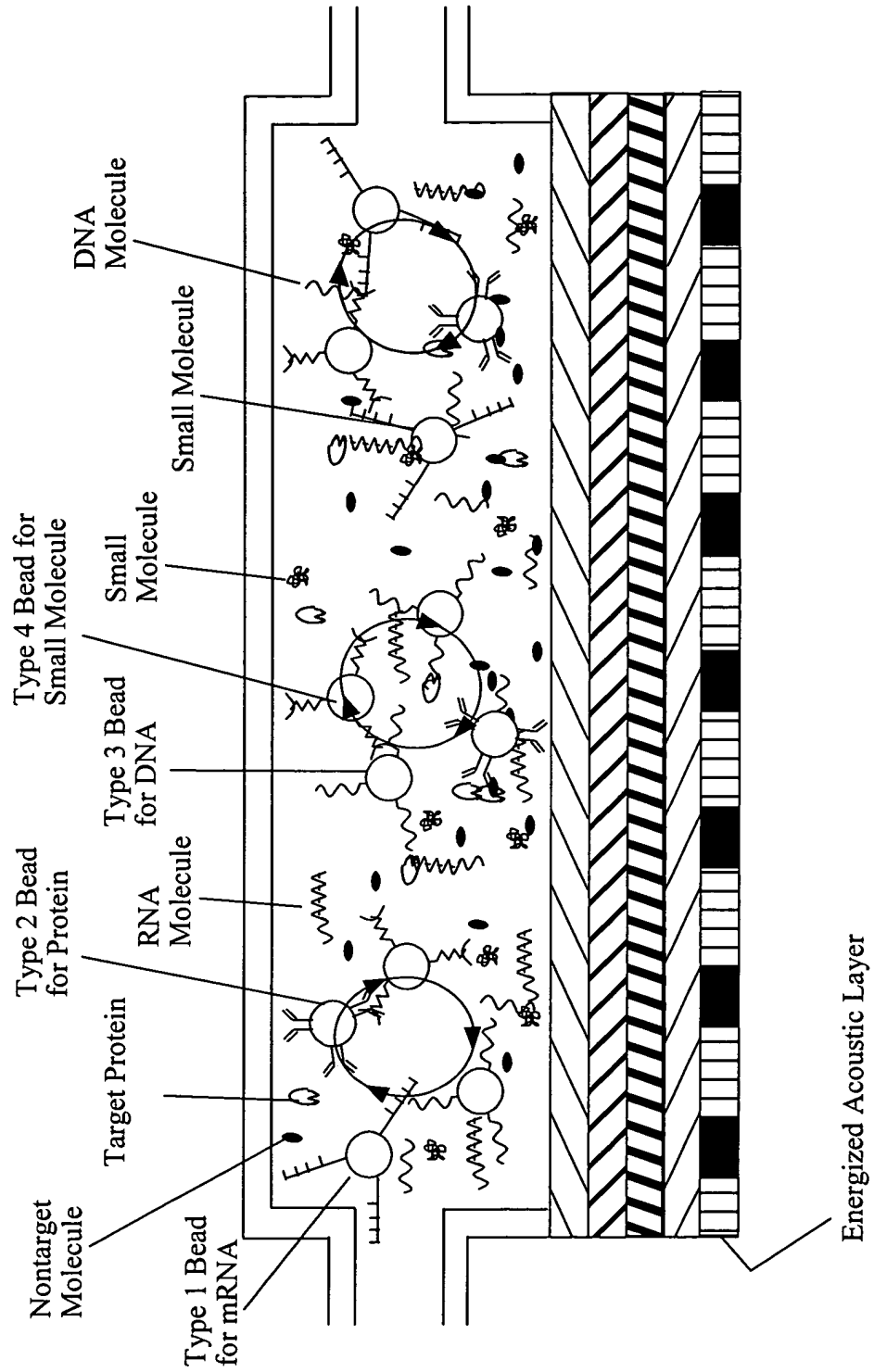
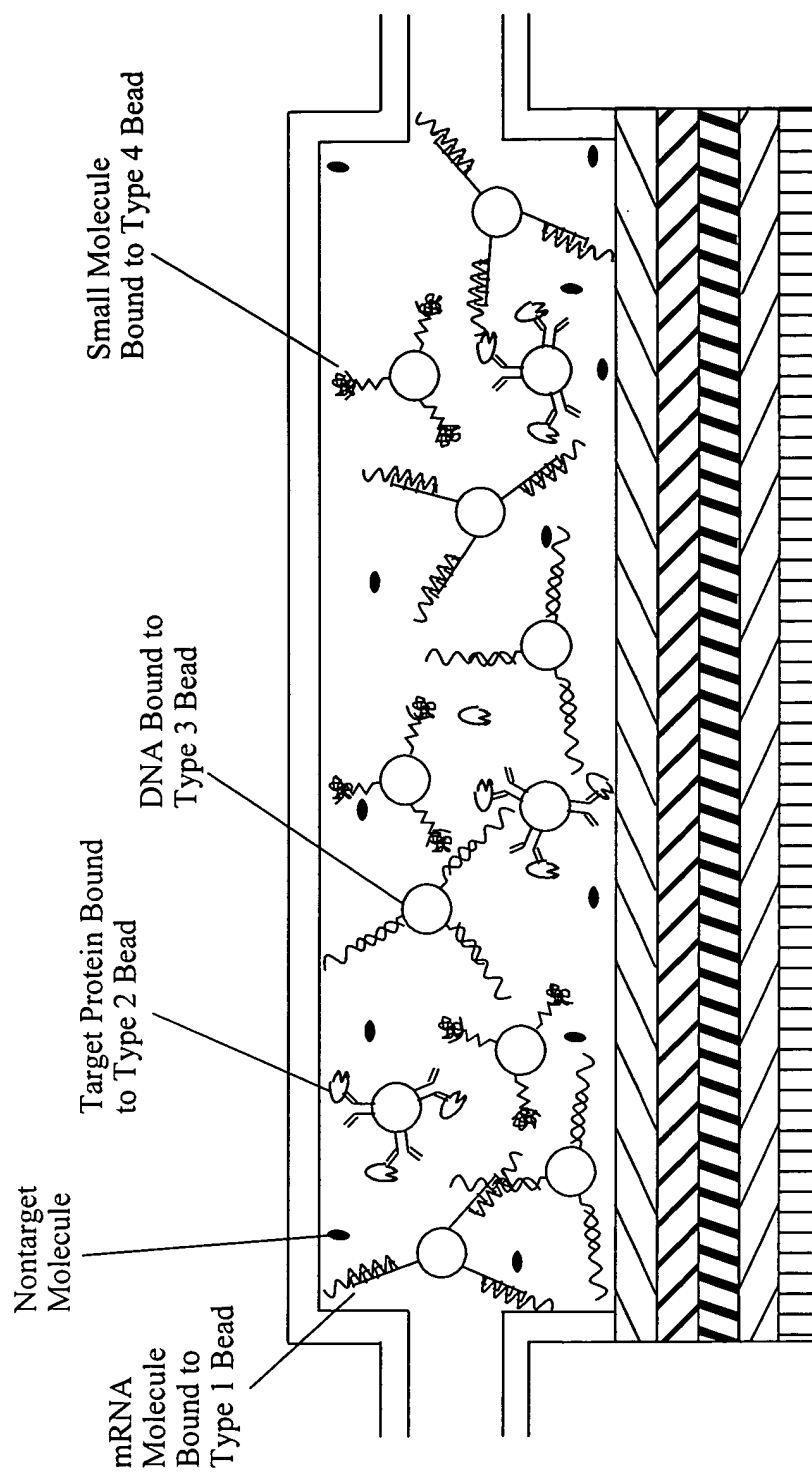


Figure 11



1. The first step is to identify the problem or goal. This involves understanding the current situation and what needs to be achieved.

Figure 12A

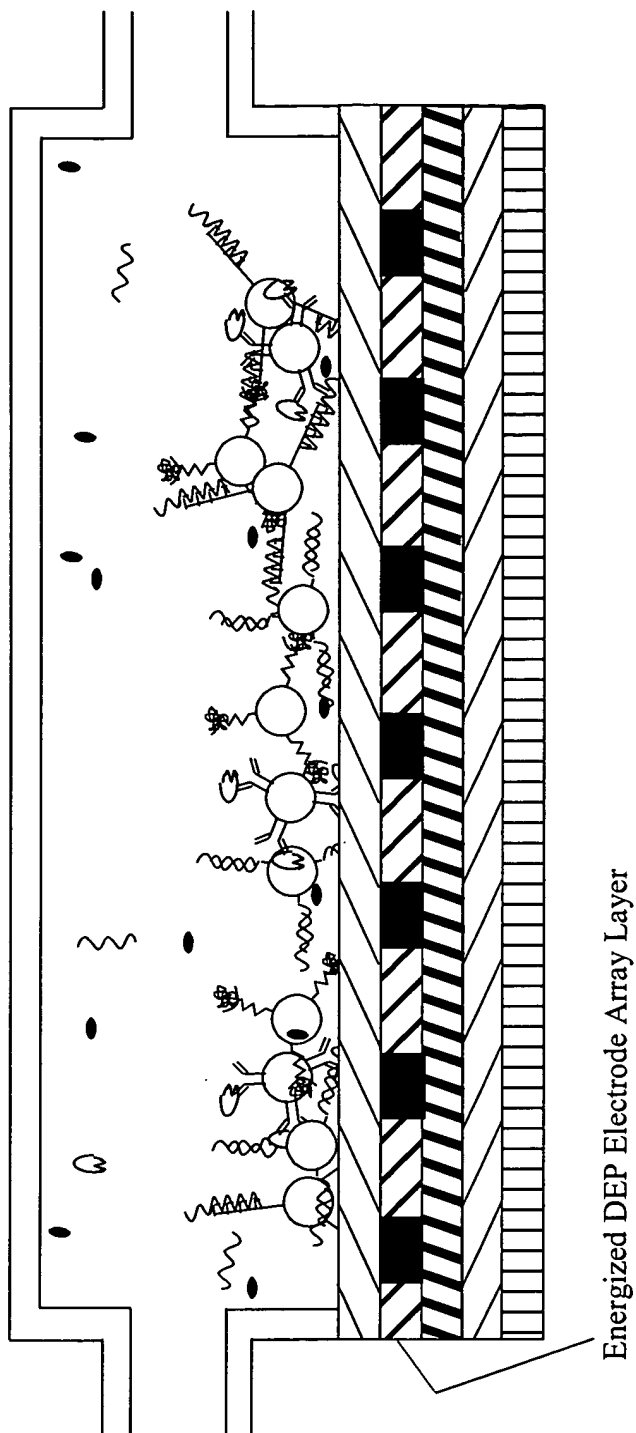
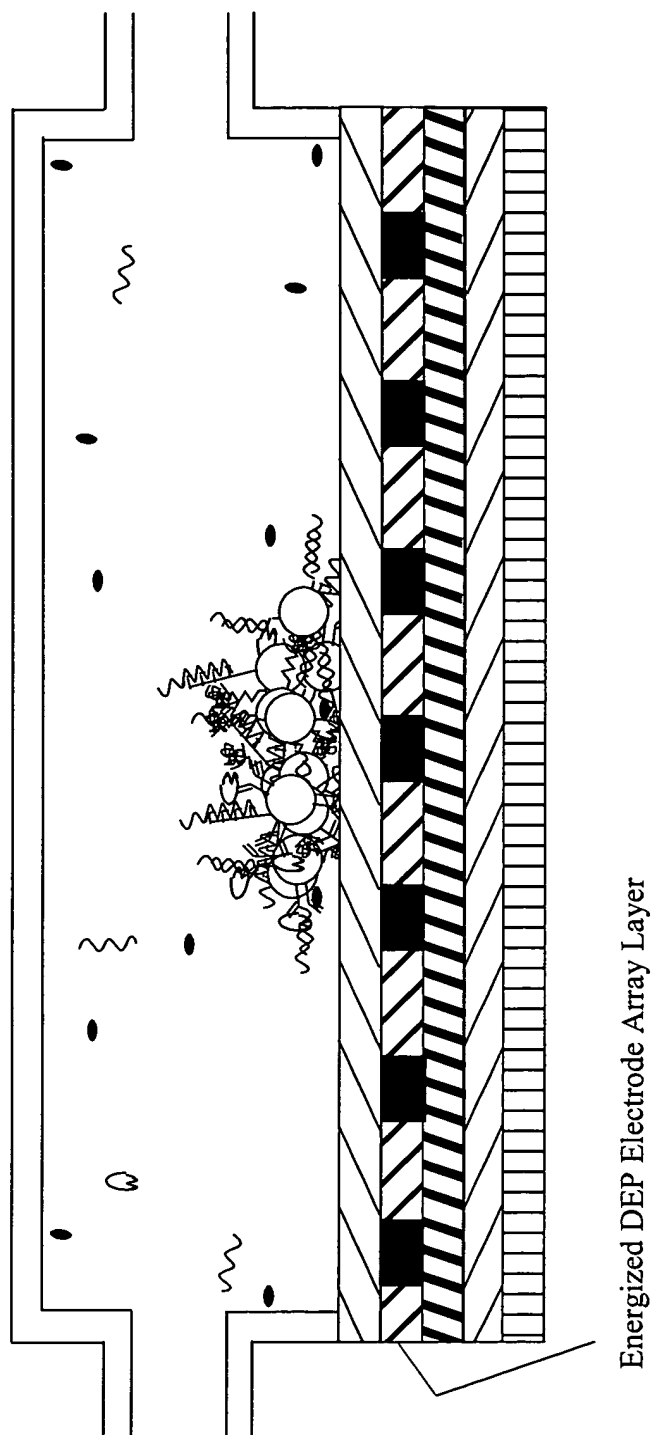
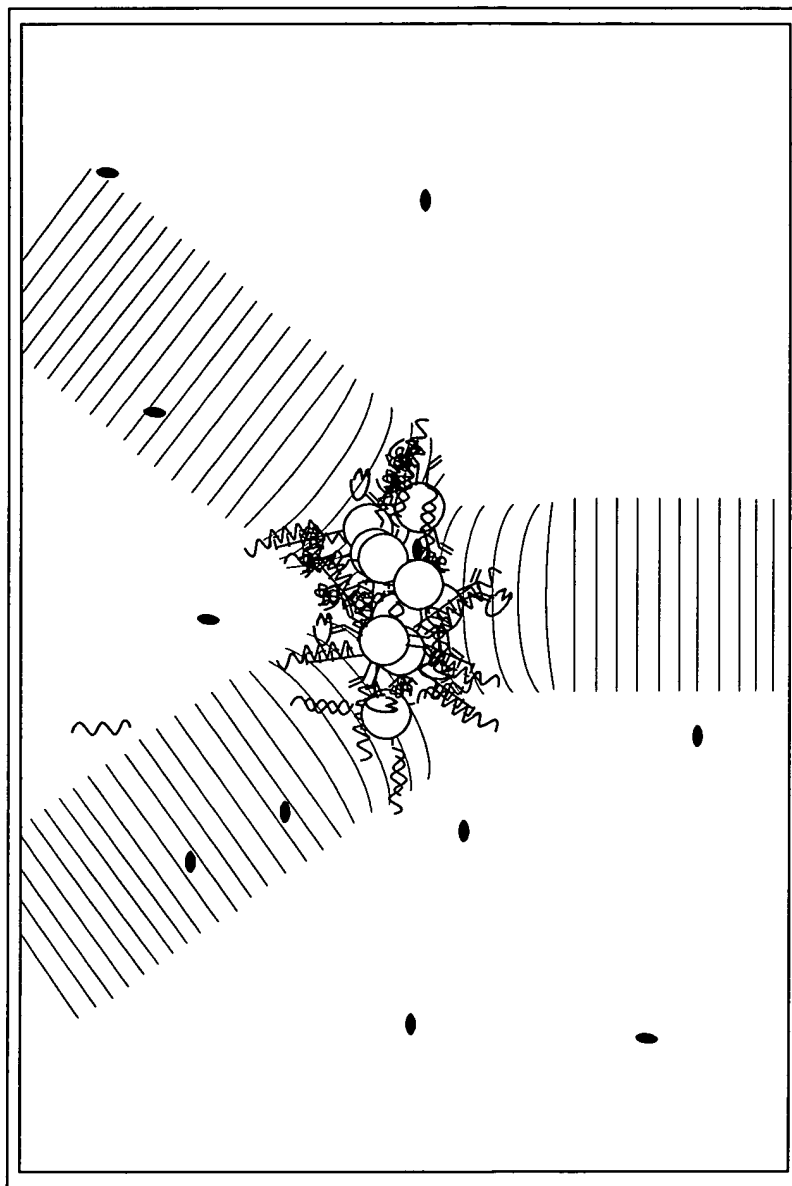


Figure 12B



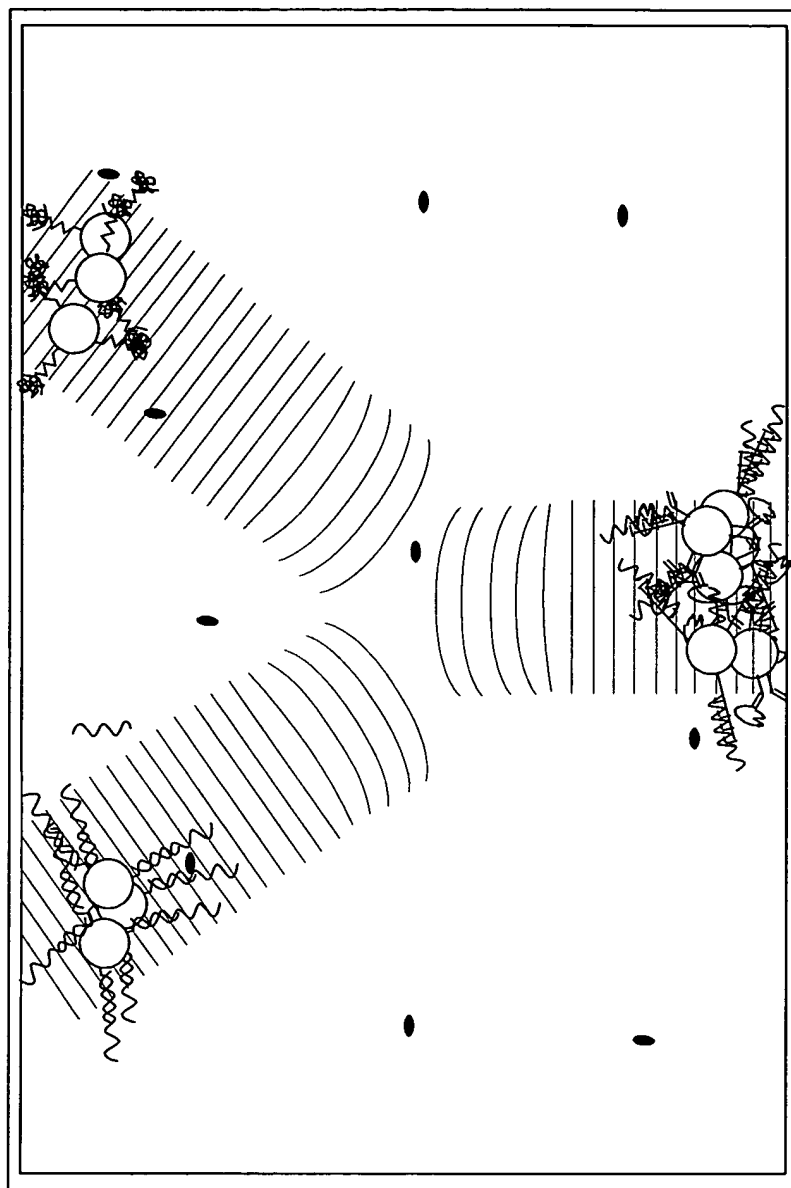
TOP SECRET

Figure 13A



TECHNICAL

Figure 13B



TOP SECRET

Figure 13C

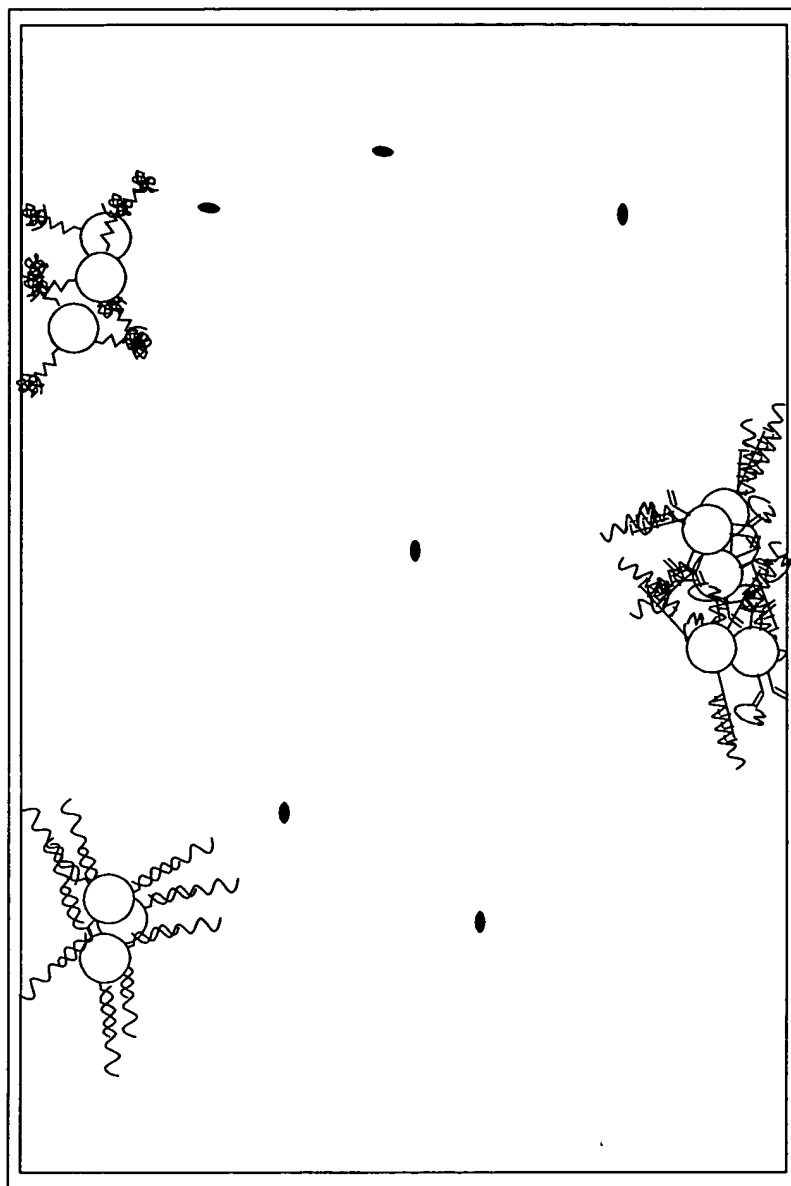


Figure 14A

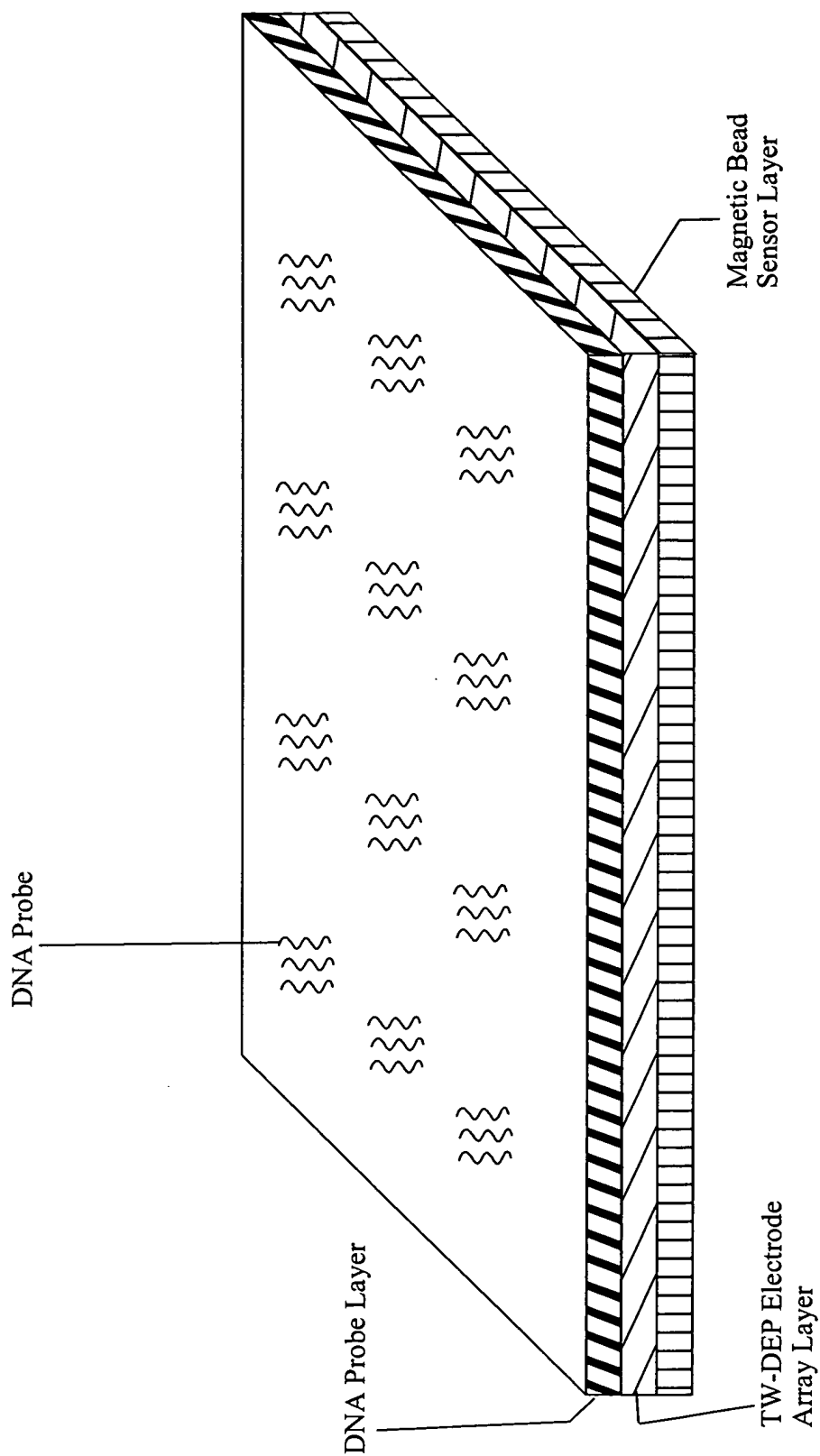


Figure 14B

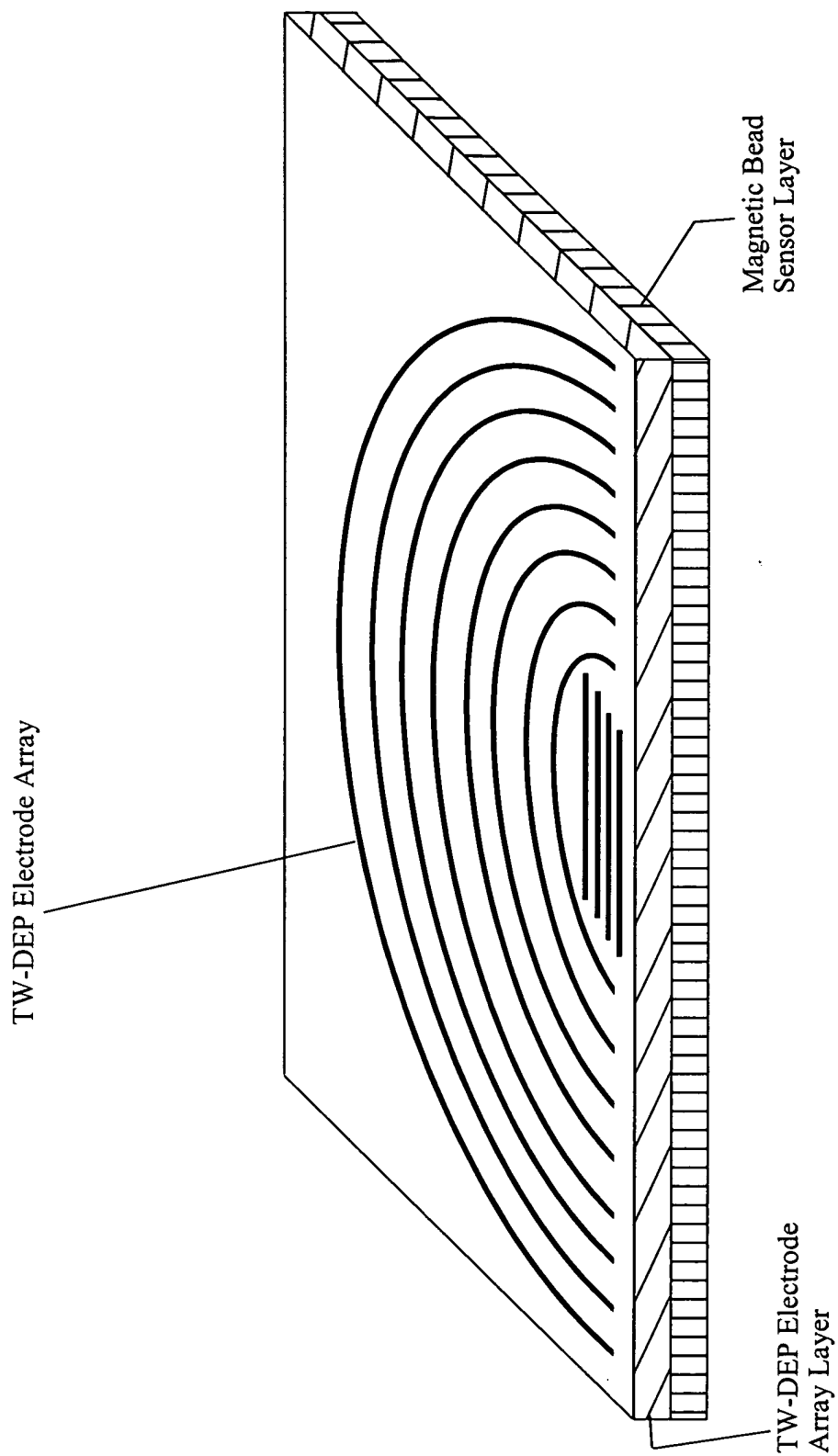


Figure 14C

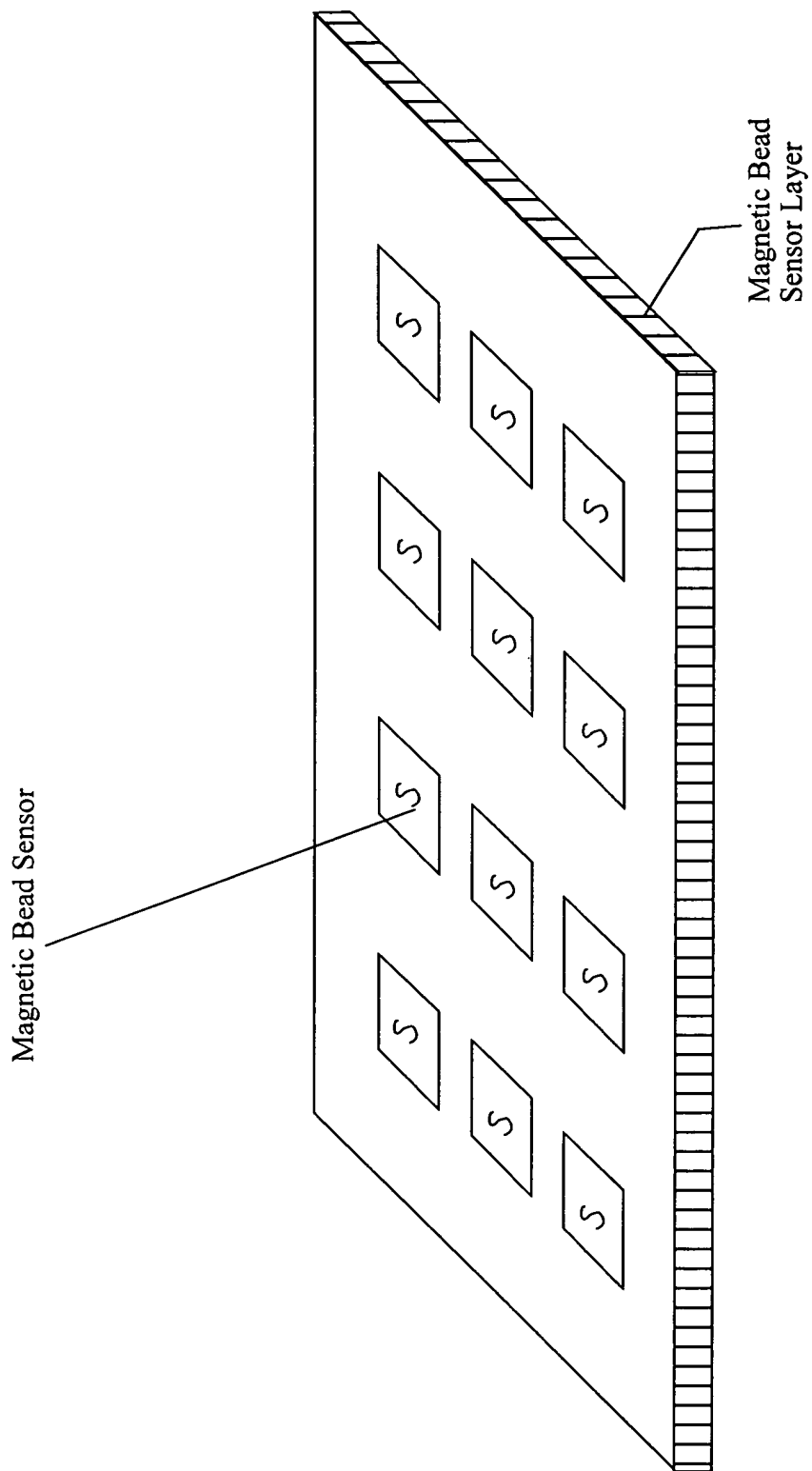


Figure 14D

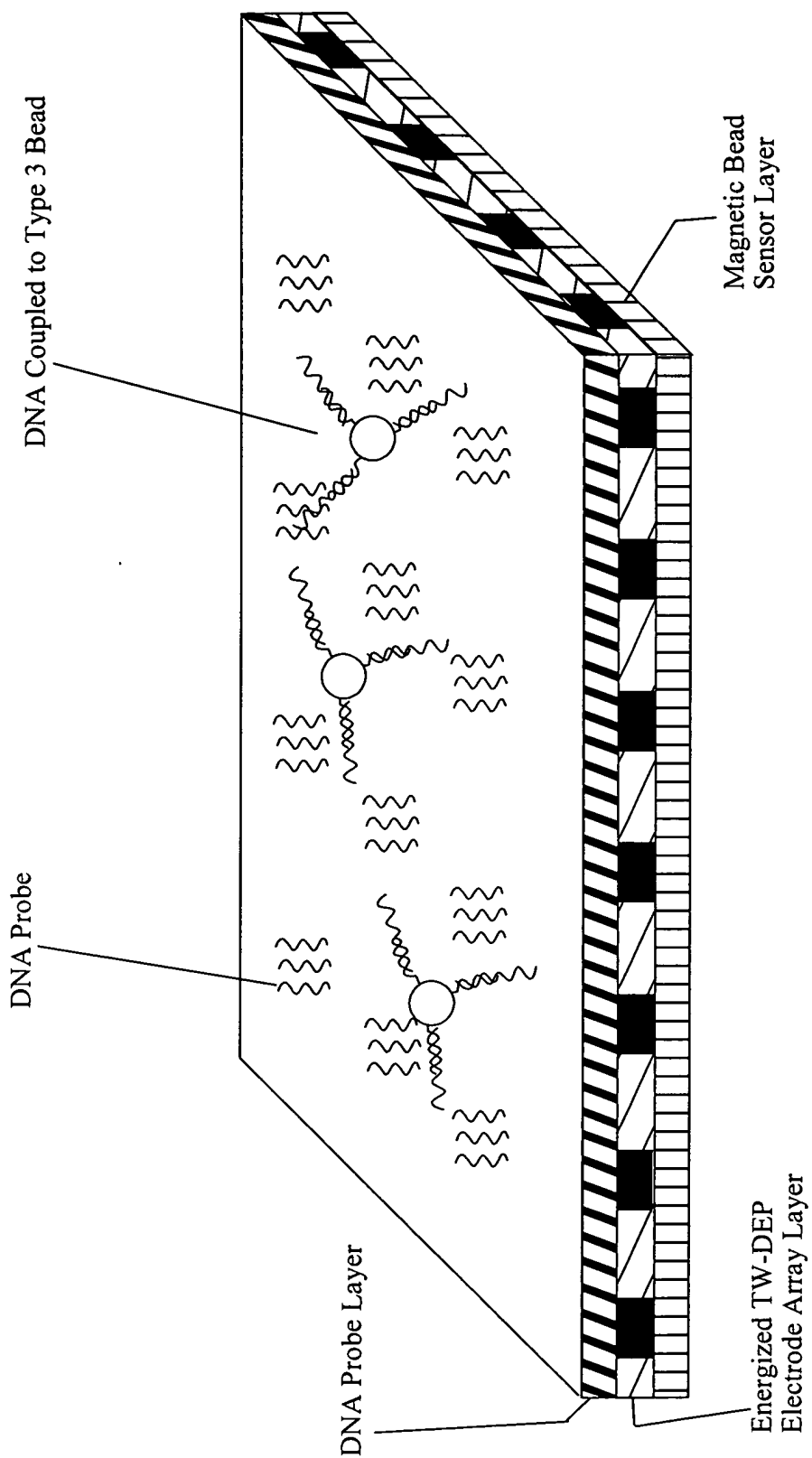


Figure 14E

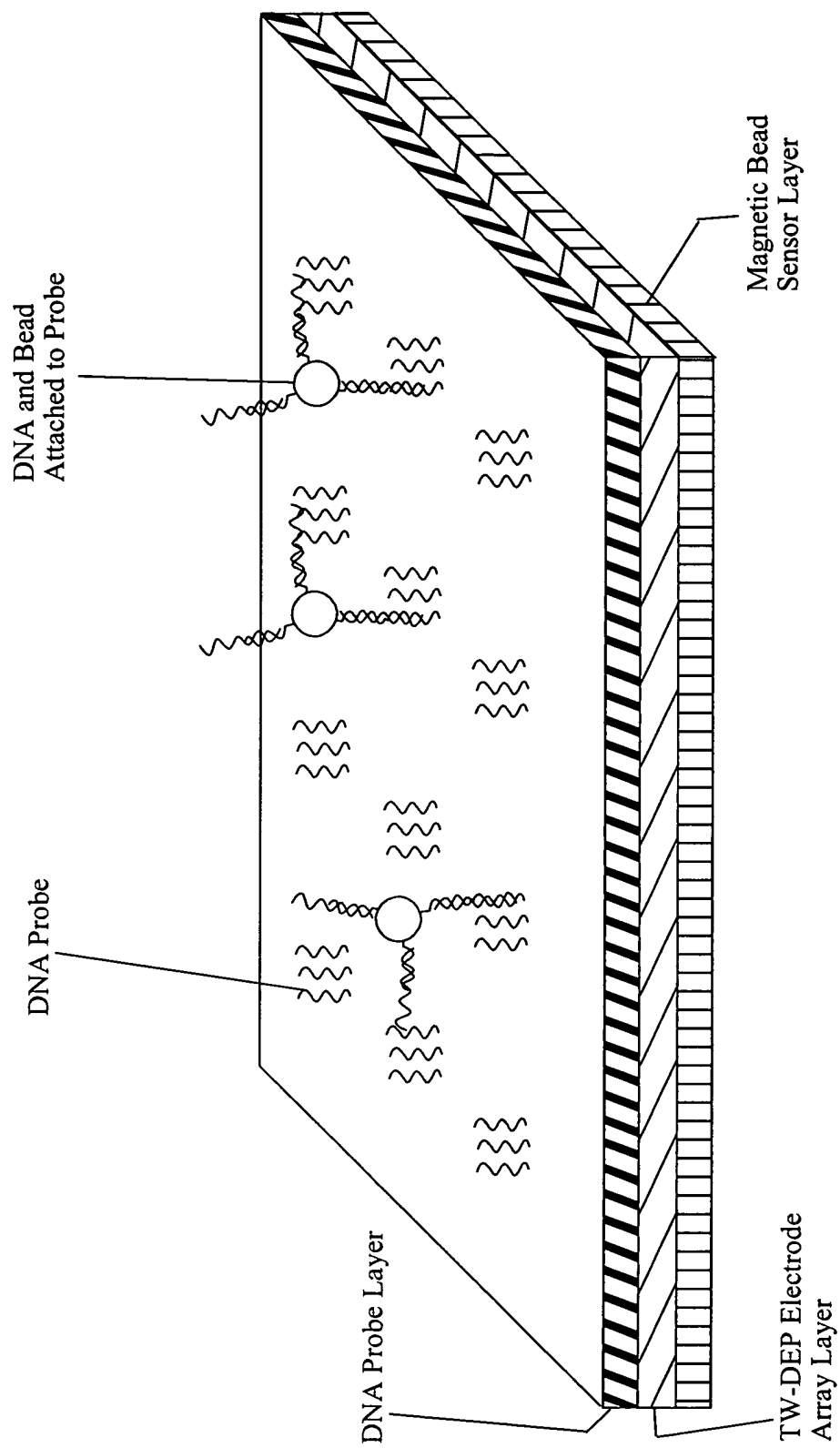


Figure 14F

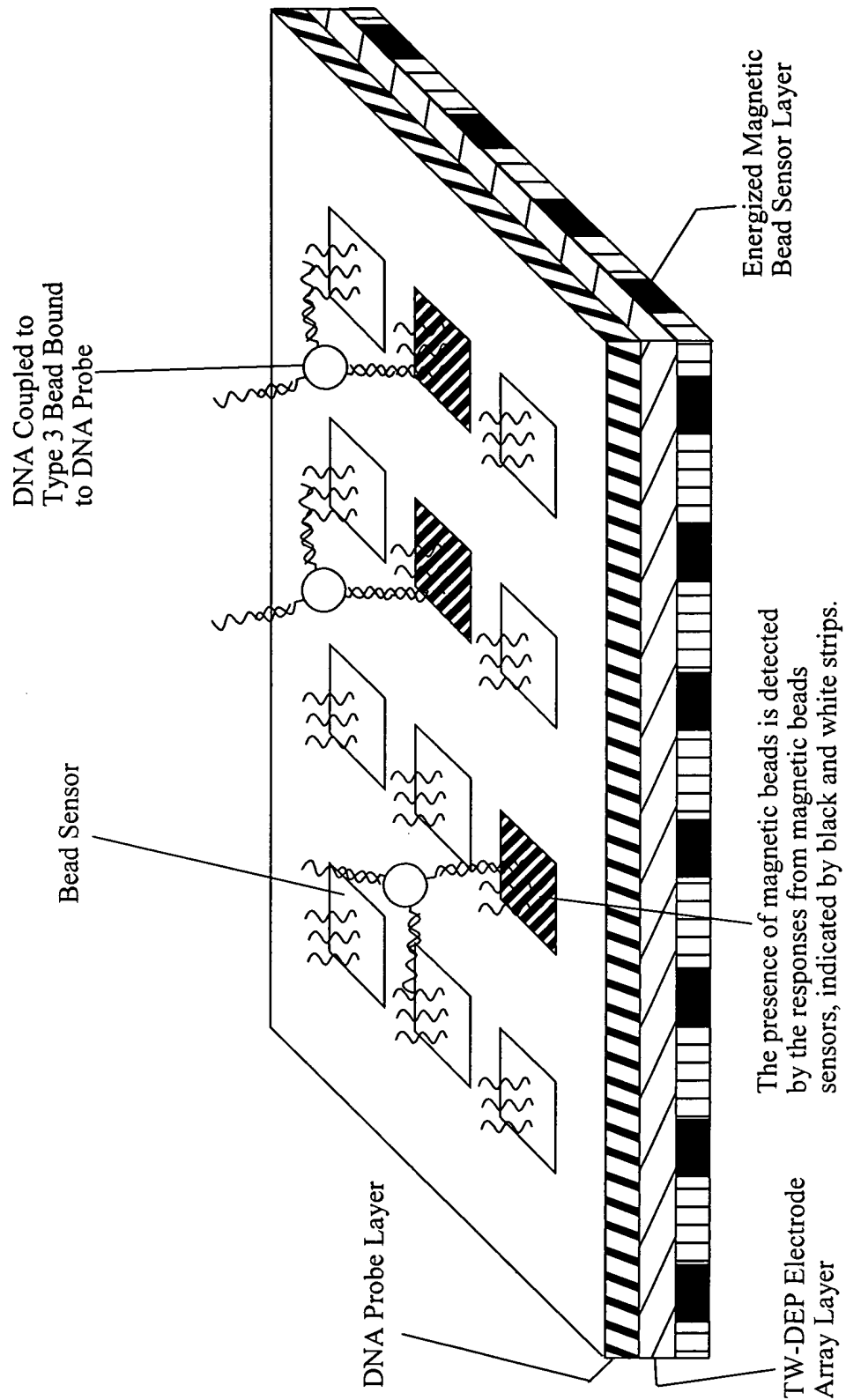


Figure 15A

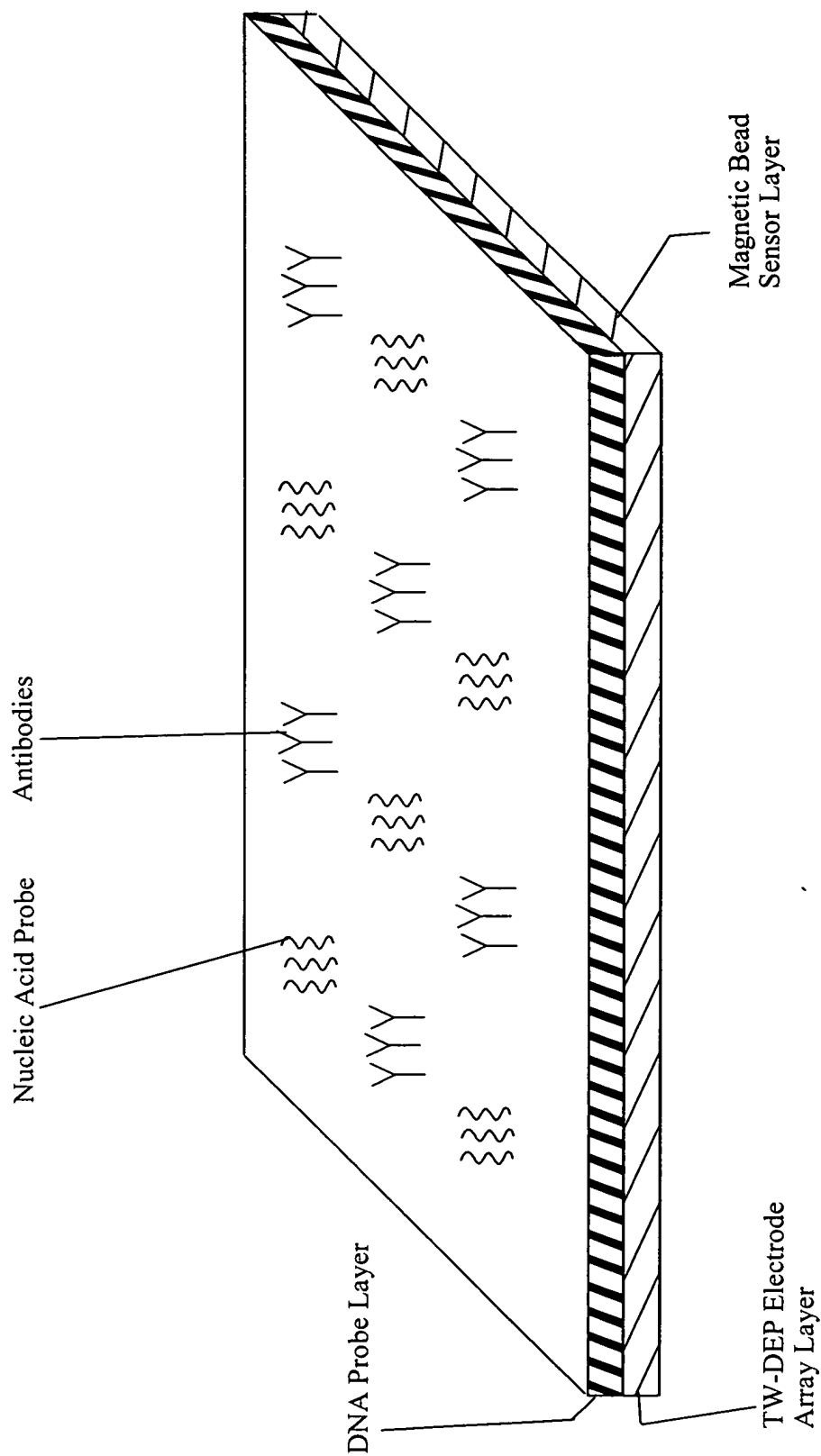


Figure 15B

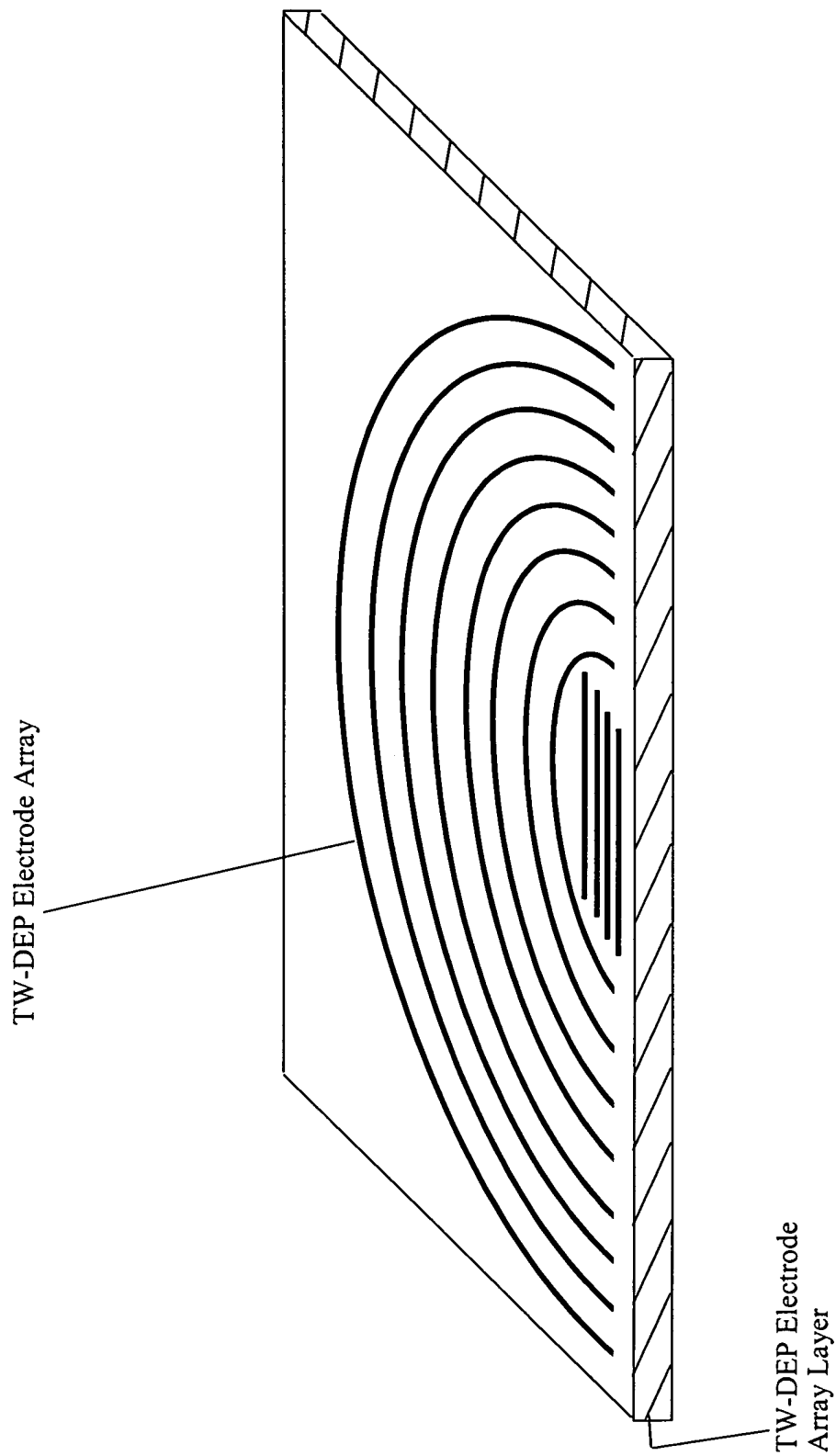


Figure 15D

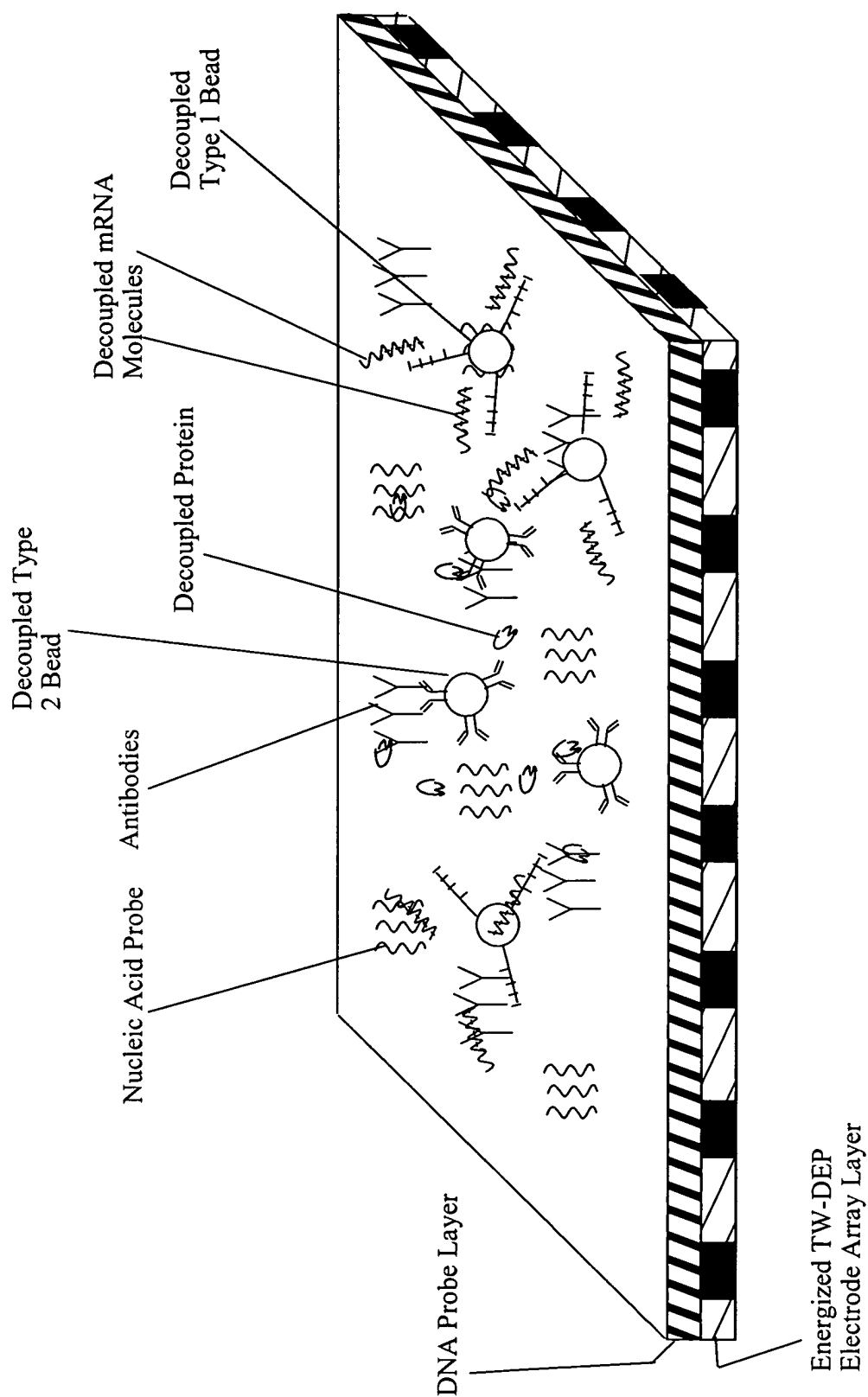


Figure 15E

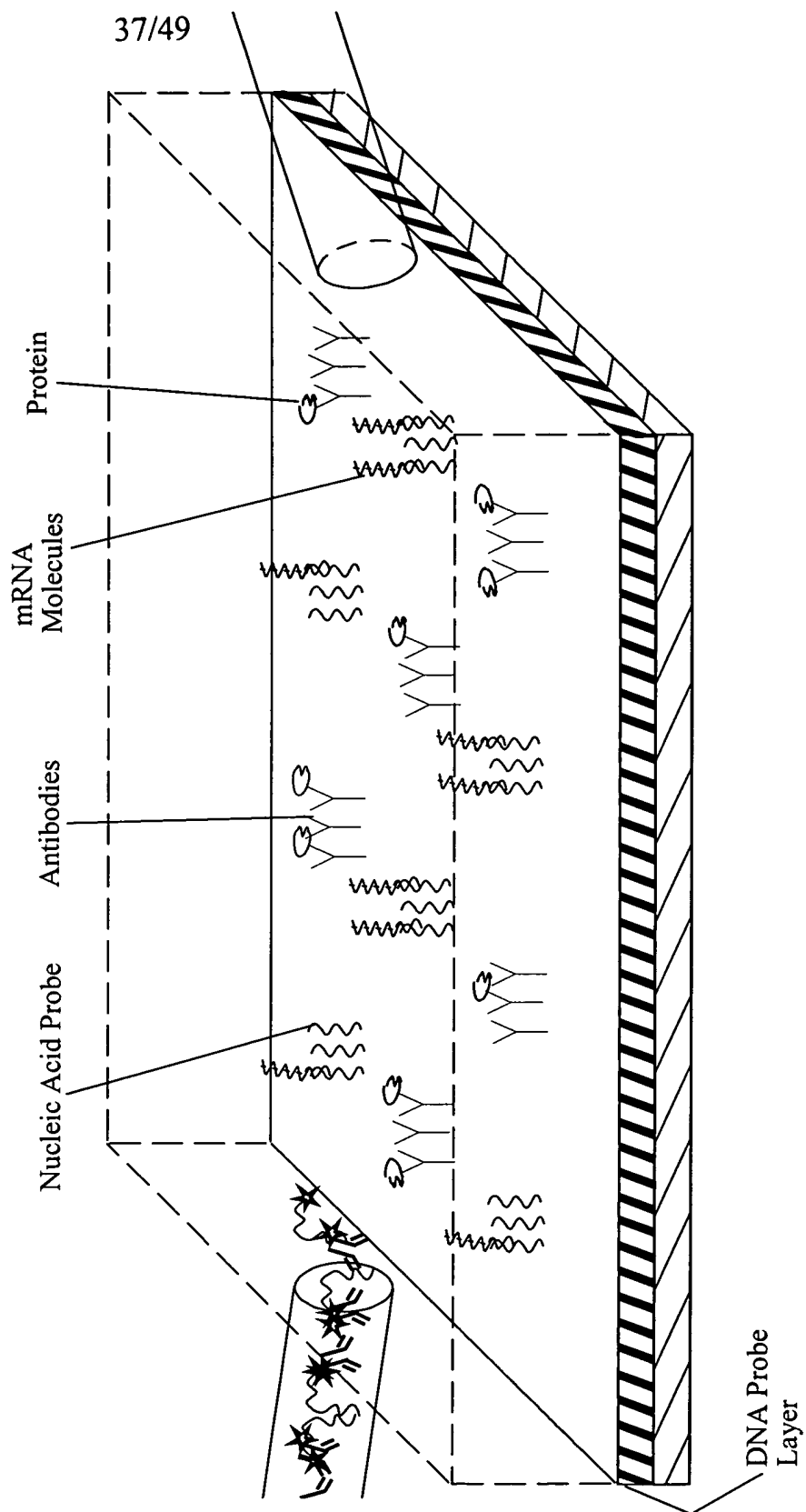


Figure 15F

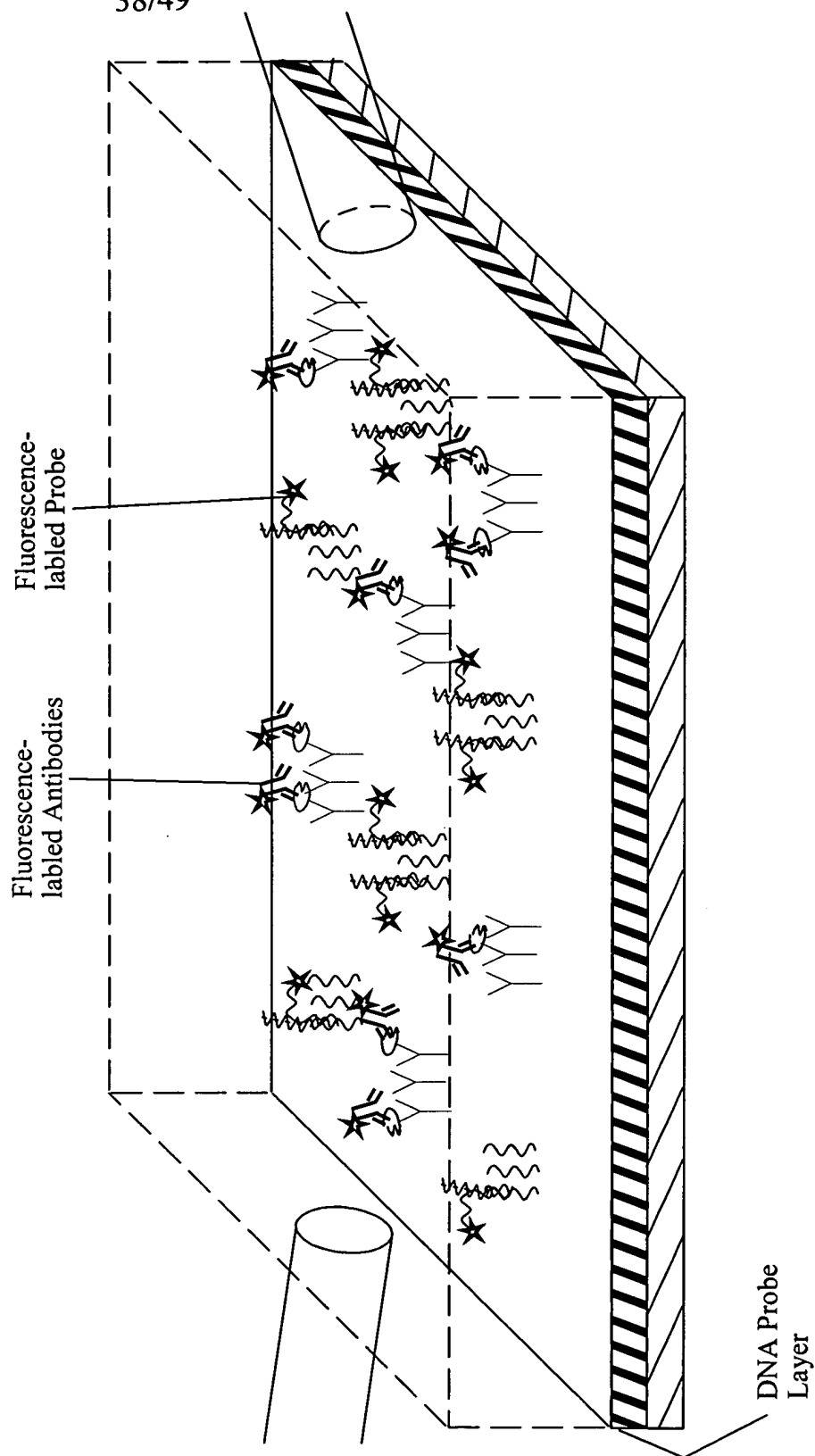


Figure 16A

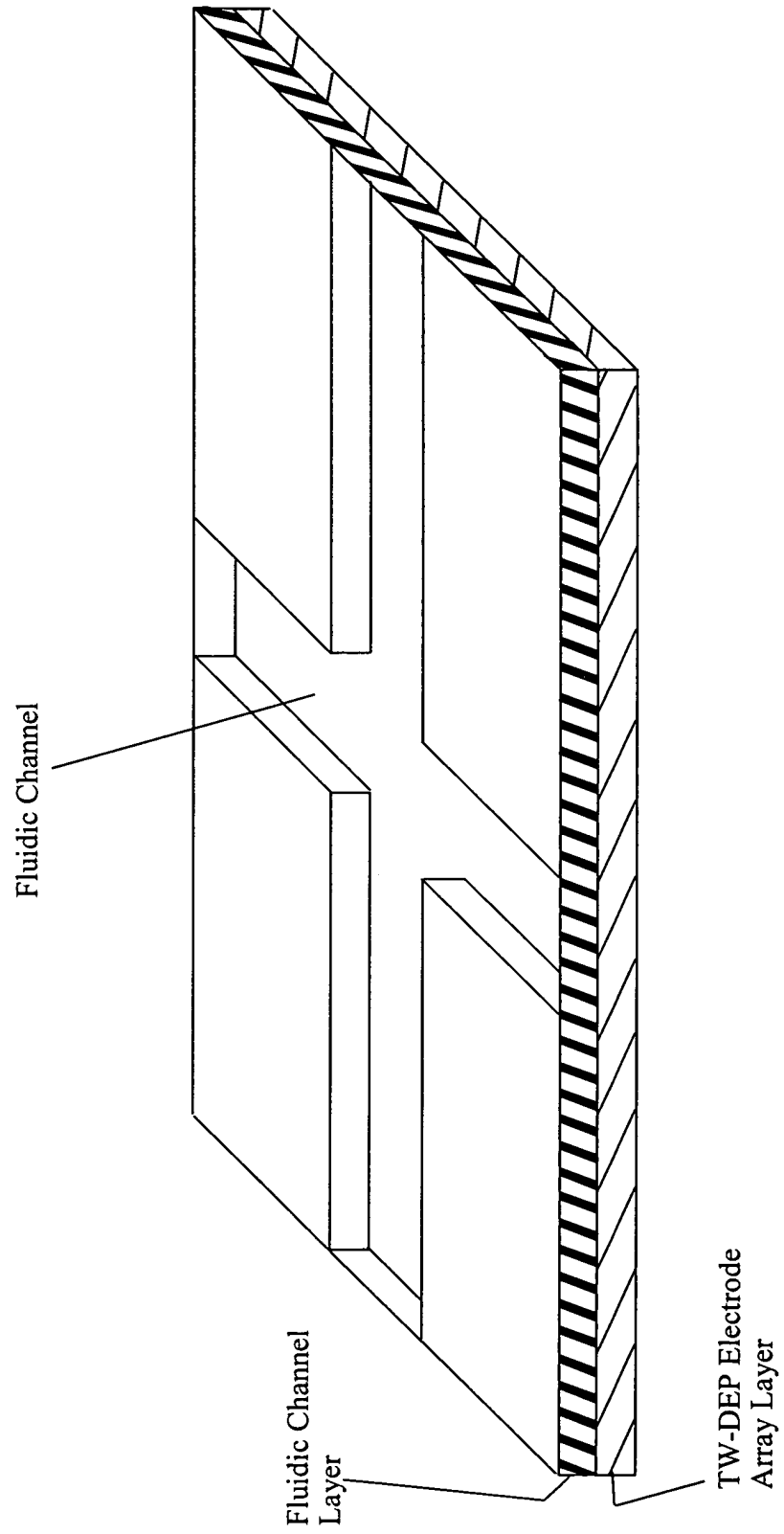


Figure 16B

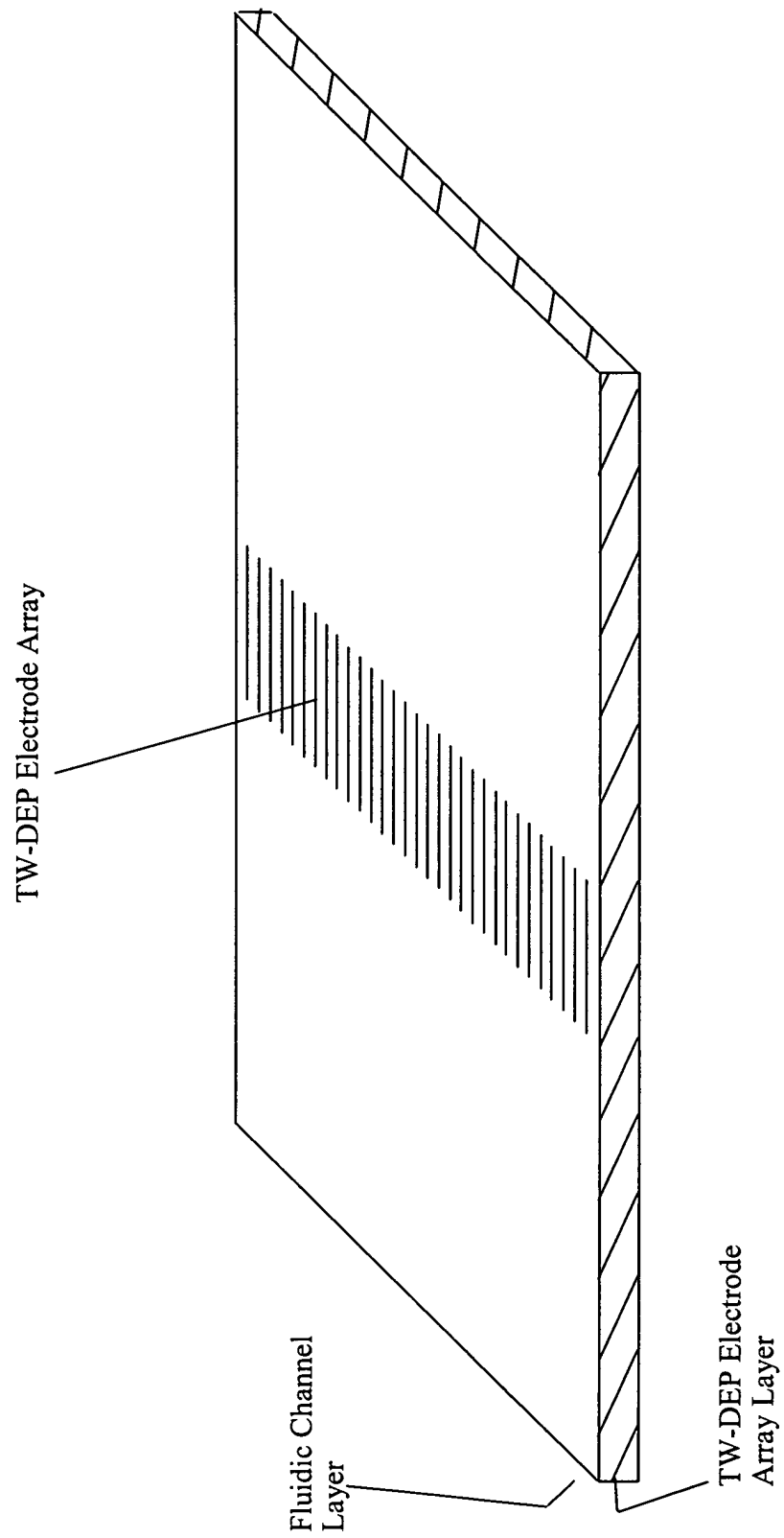


Figure 16C

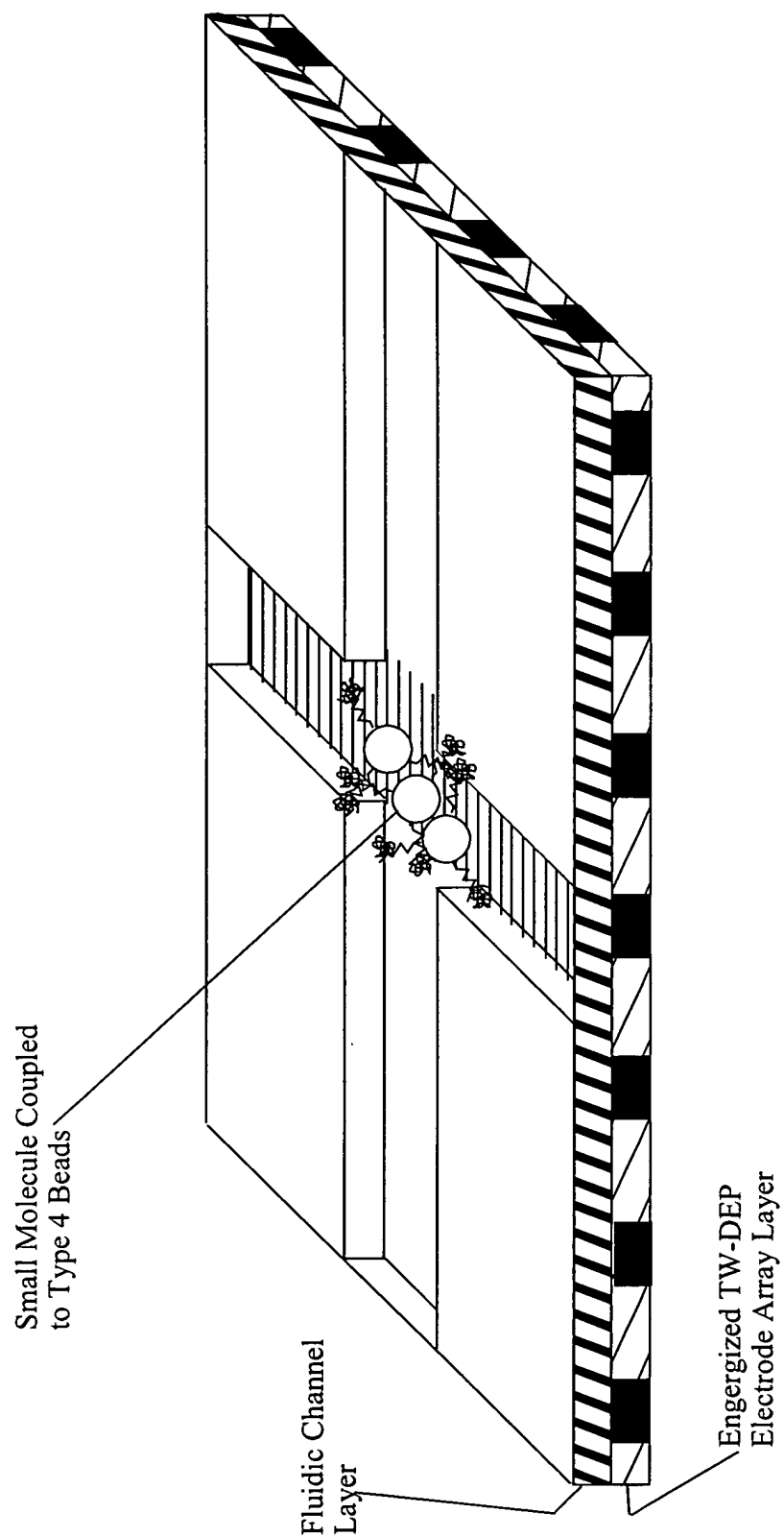


Figure 16D

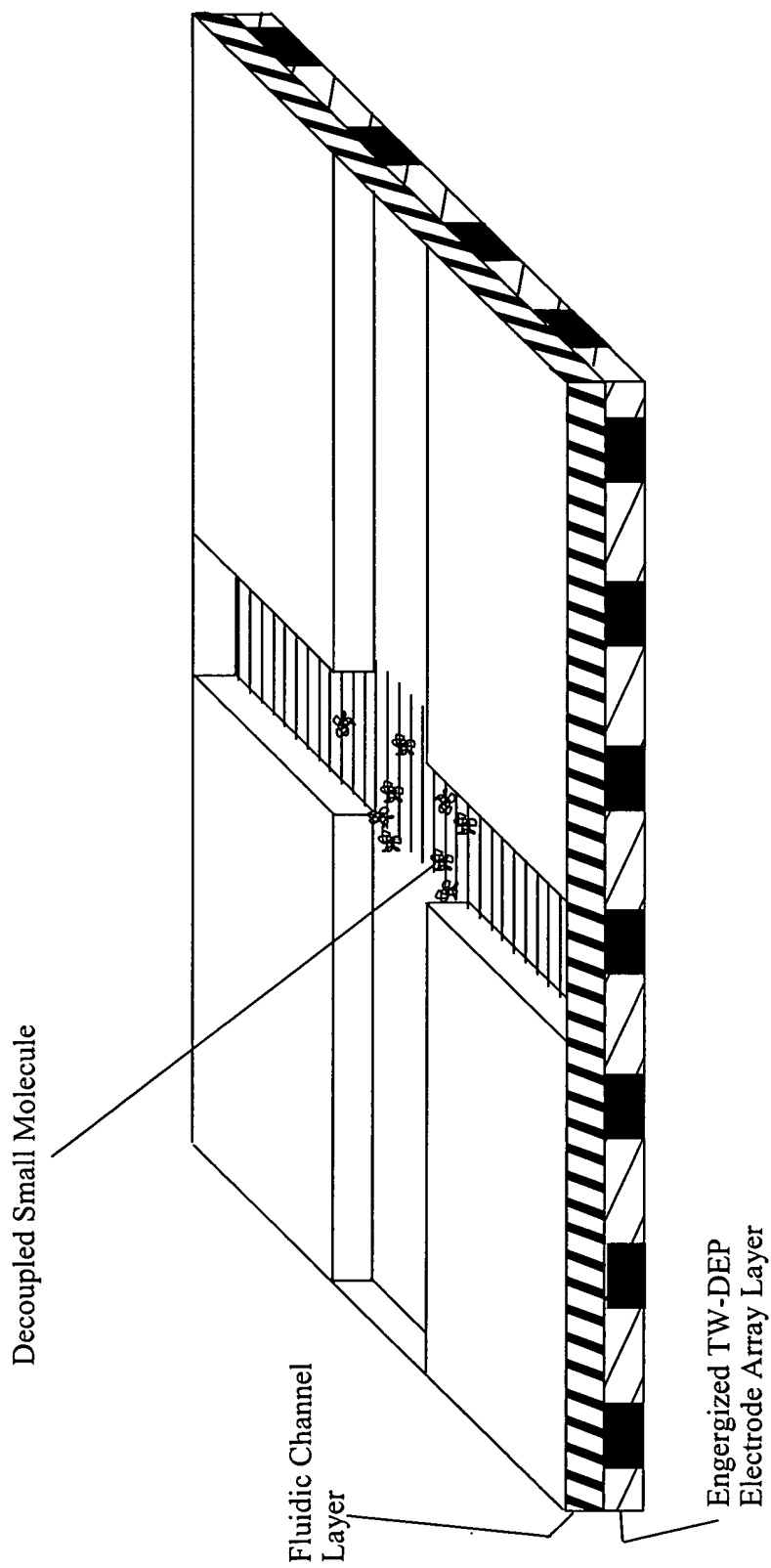


Figure 16E

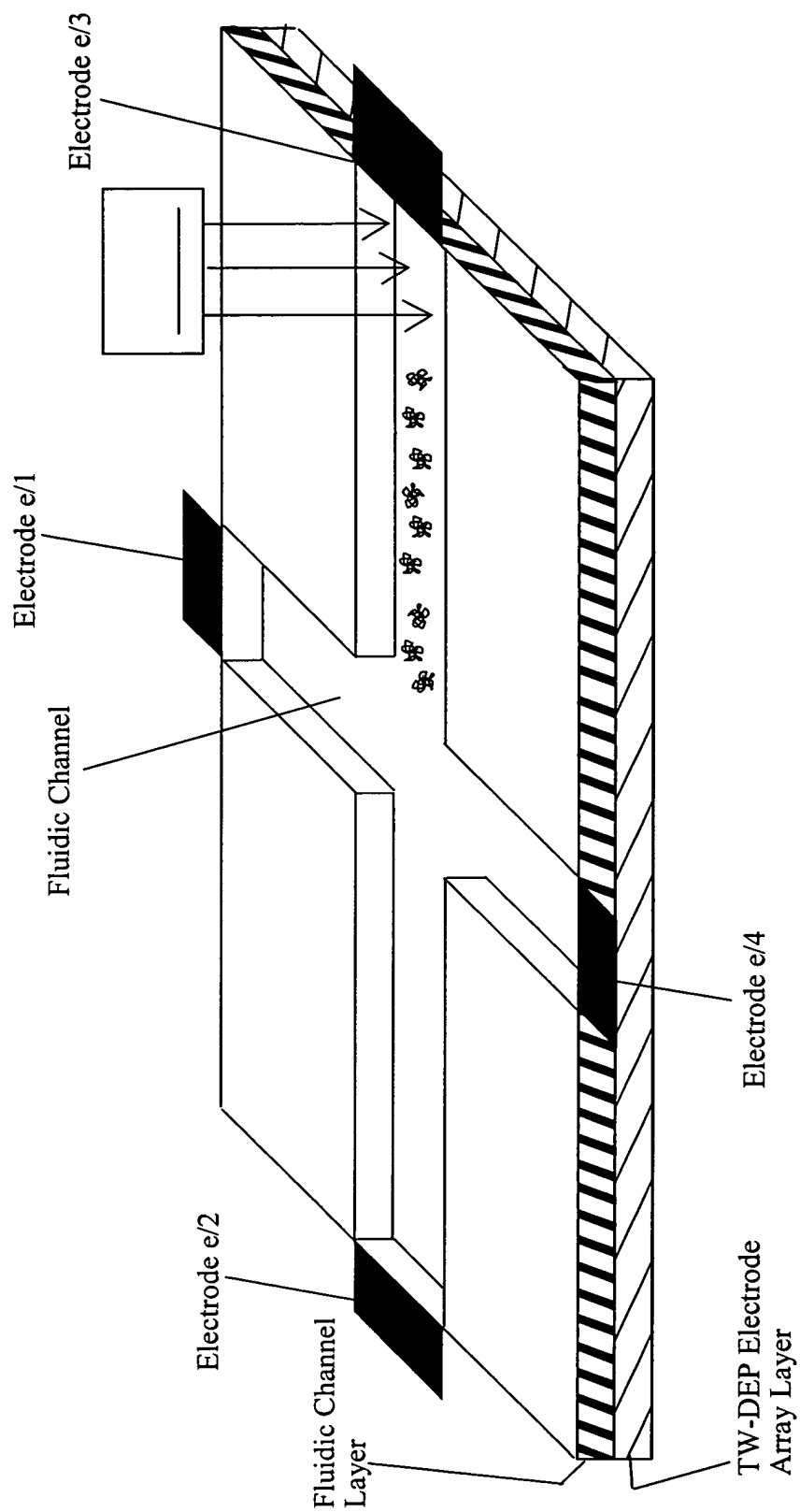


Figure 16F

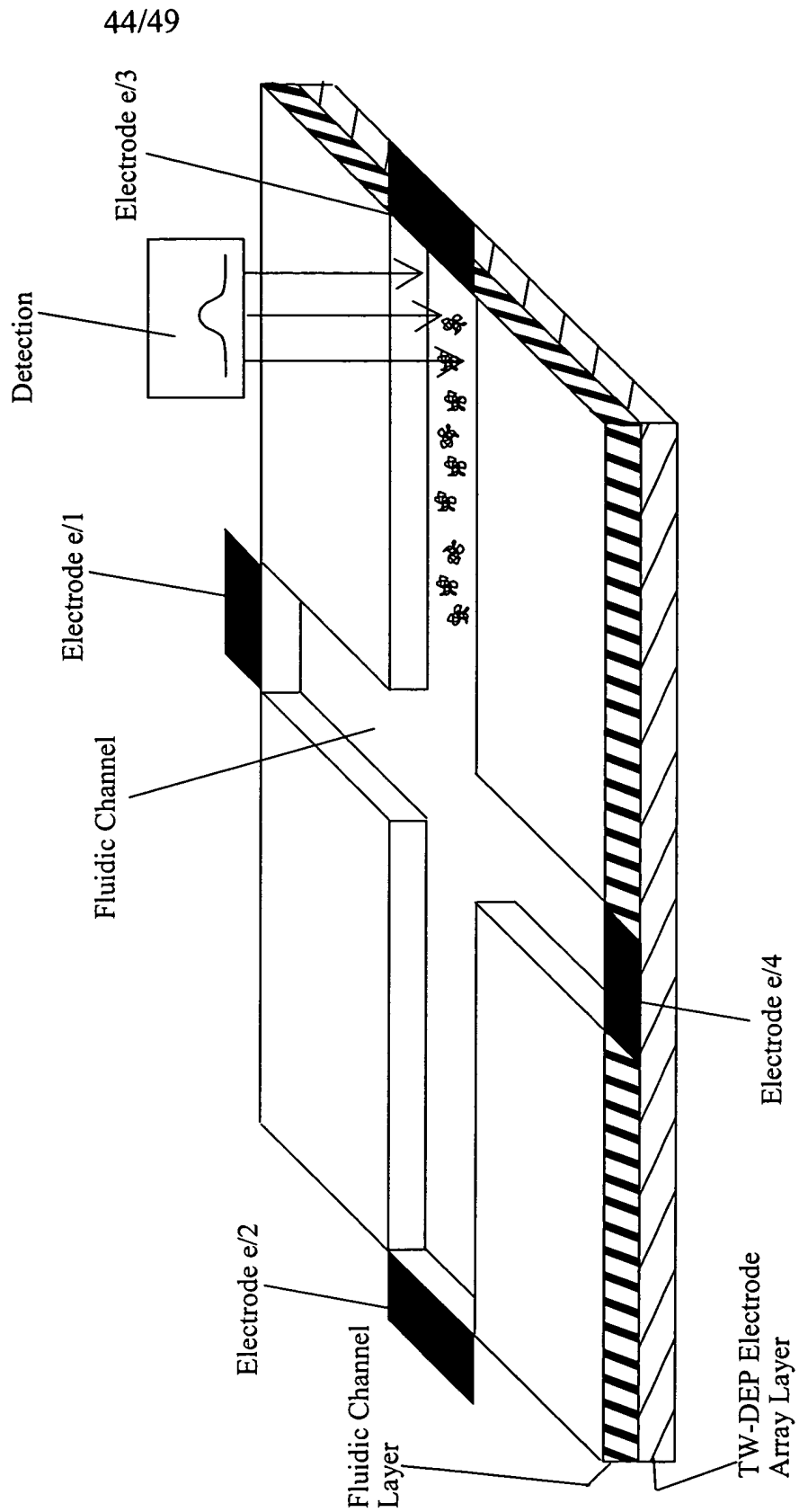
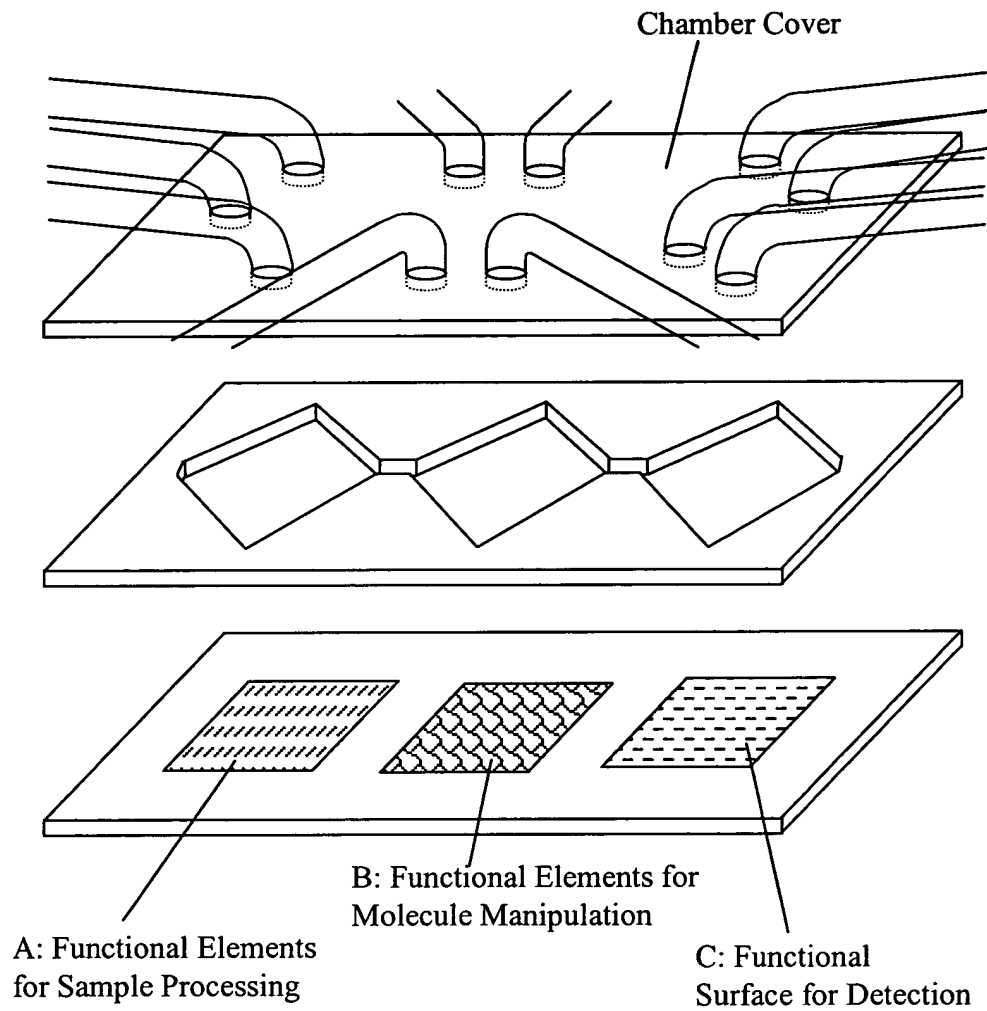


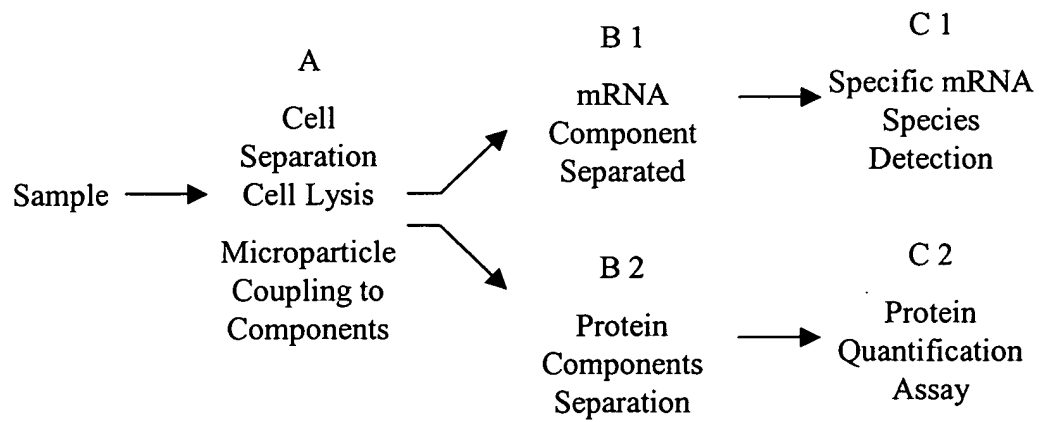
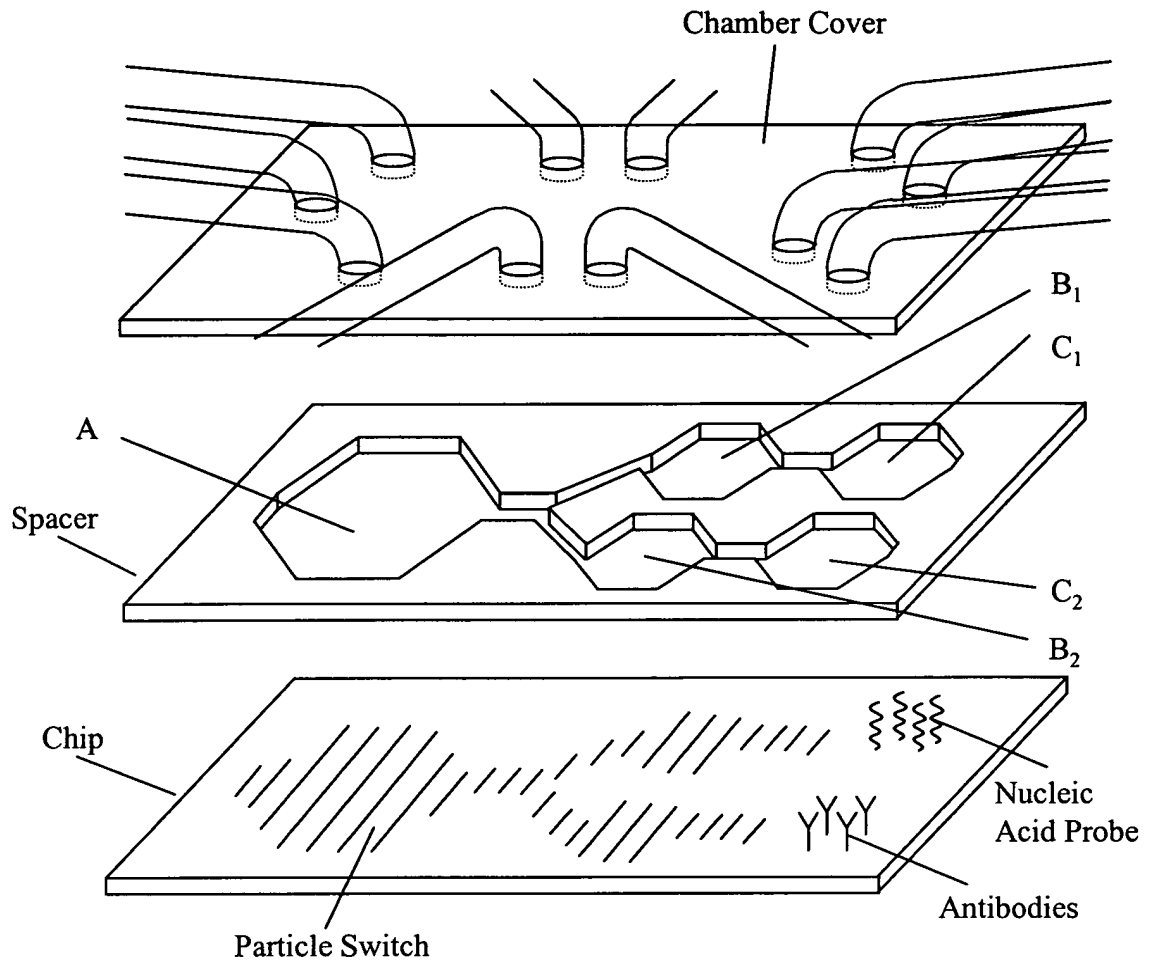
Figure 17

SINGLE CHIP SYSTEM



TOP SECRET

Figure 18



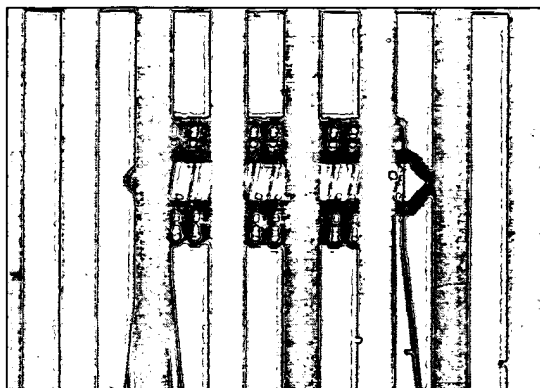


Figure 19A

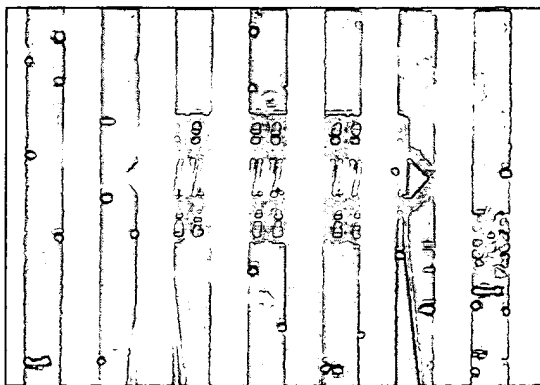


Figure 19B

TOP SECRET 636260



Figure 19C



Figure 19D

FOOT-623660

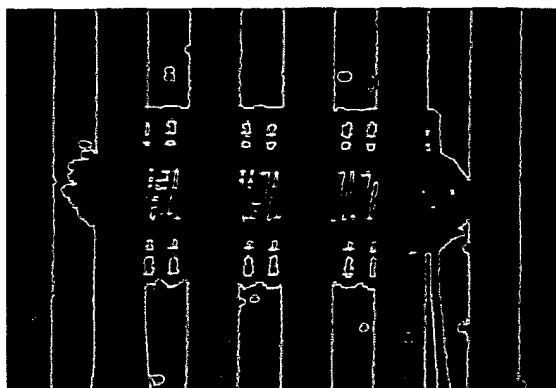
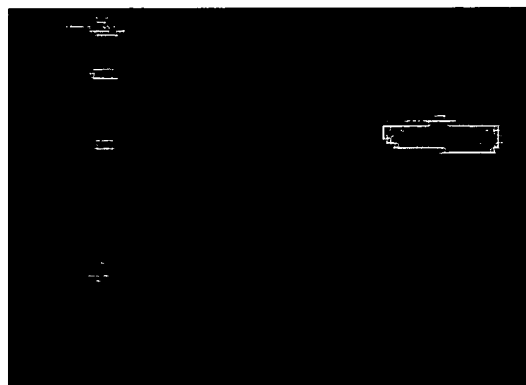


Figure 19E



Marker N-control G3PDH

Figure 19F

FOOT-626260